# 정전력 변환시스템에서 정현파 입력전류를 얻기위한 전류주입형 인터페이스 리액터

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# A New Active Interphase Reactor with Sinusoidal Input Current in the Static Power Converter System

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Abstract - In this paper, a new active interphase reactor for twelve-pulse diode rectifiers is proposed. The proposed system draws near sinusoidal currents from the utility. In this scheme, a low kVA (0.02 P<sub>0</sub> (PU)) active current source injects a triangular current into an interphase reactor of a twelve-pulse diode rectifier. The modification results in near sinusoidal input current with less than 1% THD. Experimental results are provided from a 208V, 10kVA rectifier system.

### I. Introduction

Large harmonics, poor power factor and high total harmonic distortion (THD) in the utility interface are common problems when nonlinear loads such as adjustable speed drives, power supplies, induction heating systems, UPS systems and aircraft converter systems are connected to the electric utility. In several cases, the interface to the electric utility is processed with a three phase uncontrolled diode bridge rectifier. Due to the nonlinear nature of the load, the input line currents have significant harmonics. For adjustable speed ac motor drive systems with no de-link smoothing inductor, the discontinuous conduction of the diode bridge rectifier results in a high THD and can lead to the malfunction of sensitive electronic equipment. The recommended practice, IEEE 519, has evolved to maintain utility power quality at acceptable levels [1].

A number of methods have been proposed to overcome the presented problems [2-12]. One approach is to use a conventional twelve-pulse diode rectifier which requires two six-pulse diode rectifiers connected via Y- $\Delta$  and Y-Y isolation transformers. An interphase reactor is required to ensure the independent operation of the two parallel-connected three-phase diode bridge rectifiers. The operation of the conventional twelve-pulse diode rectifier results in the cancellation of the 5th and 7th harmonics in the input utility line currents.

To increase the pulse number further to 18 or 24, additional diode bridge rectifiers along with complicated multiphase transformer arrangements become necessary, which adds to the cost and complexity.

This paper proposes a new three-phase diode rectifier system which draws near sinusoidal input currents from the three phase electric utility. In this scheme ( Fig. 1), a  $\Delta-Y$  isolation transformer of 0.52  $P_o$  (PU) capacity is employed. The interphase reactor and the line impedances  $L_{a1}$ .  $L_{a2}$  are designed such that stable twelve-pulse operation is obtained with equal current sharing. A low kVA( 0.02  $P_o$  (PU) ) PWM-controlled active current source,  $I_a$  is injected into the secondary winding of the interphase reactor. It is shown via rigorous mathematical modeling as well as computer simulations that the exact shape of  $I_a$  (Fig. 2 (a)) can be computed to alter the utility line current  $I_a$  to a perfect sinewave. It is further shown that an approximation to the exact waveshape of  $I_a$  is a triangular wave. Therefore by injecting a triangular shaped current  $I_a$  into the secondary winding of the interphase reactor, near sinusoidal input line currents flow in the utility line with less than 1% THD.

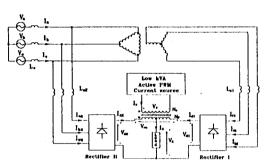


Fig. 1 Circuit diagram of the proposed clean power utility interface.

### II. Proposed clean power utility interface

Fig. 1 shows the circuit diagram of the proposed scheme to shape input line currents. The main transformer has delta-wye winding with a  $\sqrt{3}$  to 1 turns ratio to maintain an equal per unit voltage. They are connected in such a way that the two diode bridge rectifiers have balanced sets of three-phase voltages with 30 degrees phase shift. The proposed system is identical to a conventional 12-pulse system except that the interphase reactor has an additional winding. The additional winding is used to inject a low kVA PWM current source to shape input line current.

With the PWM current source,  $l_{xx}$  disabled (i.e.  $l_{x}=0$ ) the system operates as a conventional 12-pulse rectifier providing cancellation of the 5th and 7th harmonics in the input line currents,  $l_{xx}$ ,  $l_{yx}$  and  $l_{xx}$ . The active current source  $l_{xx}$ , when injected into the interphase reactor, results in near sinusoidal input current with unity input power factor. The following sections illustrate the proposed concept in more detail.

# A. Analysis of the proposed active interphase reactor

Fig. 1 shows the proposed active interphase reactor for a 12-pulse diode rectifier. In this section, waveforms are analyzed to determine the relationship between current  $I_x$  and input currents  $I_b$ ,  $I_b$  and  $I_c$ . With  $I_x = 0$ , input current  $I_b$  can be shown to be [2,5],

$$I_a = I_{a1} + \frac{1}{\sqrt{3}}(I_{a1} - I_{a1})$$
 (1)

Equation (1) describes a 12-pulse input line current with 5th and 7th harmonics absent and

$$I_{a} = I_{a} = \frac{1}{2}I_{a} \tag{2}$$

An active current  $l_x$  is now injected into the secondary winding of the interphase reactor as shown in Fig. 1. Fig. 3 shows the circuit topology for implementing this scheme. Analyzing the MMF relationship of the interphase reactor, we have,

$$N_{p}(1_{11} - 1_{41}) = N_{1}1_{x}$$
(3)

where  $N_p$  and  $N_s$  are the numbers of turns of the primary and the secondary windings of the interphase reactor. The load current  $I_d$  is,

$$I_4 = I_{41} + I_{42} \tag{4}$$

From (3) and (4) we have,

$$l_{st} = \frac{1}{2} (I_s - \frac{N_s}{N_s} I_s)$$
 (5)

$$I_{42} = \frac{1}{2} (I_4 + \frac{N_4}{N_4} I_4)$$
 (6)

A switching function S<sub>a1</sub> for phase "a" of Rectifier-I in Fig. 1 is defined and the Fourier series expansion for S<sub>a1</sub> is given by,

$$S_{st}(\omega t) = \frac{2\sqrt{3}}{\pi} \left( \sin \omega t - \frac{1}{5} \sin 5\omega t - \frac{1}{7} \sin 7\omega t + \frac{1}{11} \sin 11\omega t + \frac{1}{13} \sin 13\omega t \dots \right)^{(7)}$$

and for phase "b" and "c", the switching functions can be written as,

$$S_{tt} = S_{st} \angle -120^{\circ}$$

$$S_{ct} = S_{st} \angle +120^{\circ}.$$
(8)

Similarly, the switching functions for Rectifier-II with a 30 degree phase shift are,

$$S_{i,1} = S_{i,1} \angle -30^{\circ}$$

$$S_{i,2} = S_{i,1} \angle -30^{\circ}$$

$$S_{i,2} = S_{i,1} \angle -30^{\circ}.$$
(9)

The input currents for Rectifier I and II can now be expressed in terms of switching functions as,

$$\begin{bmatrix} I_{u} \\ I_{b_{1}} \\ I_{d_{1}} \end{bmatrix} = \begin{bmatrix} S_{u} \\ S_{b_{1}} \\ S_{d_{1}} \end{bmatrix} I_{d_{1}}$$

$$(10)$$

and

$$\begin{bmatrix} \mathbf{I}_{a} \\ \mathbf{I}_{ba} \\ \mathbf{I}_{a} \end{bmatrix} = \begin{bmatrix} \mathbf{S}_{aa} \\ \mathbf{S}_{ba} \\ \mathbf{S}_{aa} \end{bmatrix} \mathbf{I}_{da}$$

$$(11)$$

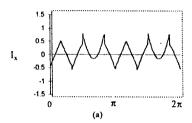
Equation (1) can now be modified using (5) and (6) and the switching functions described in (7)-(11) as,

$$I_{\star} = \frac{1}{2\sqrt{3}} (S_{\star i} - S_{\star i}) \cdot (I_{\star} - \frac{N_{\star}}{N_{\star}} I_{\star}) + \frac{1}{2} S_{\star i} (I_{\star} + \frac{N_{\star}}{N_{\star}} I_{\star})$$
(12)

Equation (12) illustrates the relationship between  $I_{\mathbf{x}}$  and input current  $I_{\mathbf{x}}$ . For input current  $I_{\mathbf{x}}$  to be sinusoidal,

$$I_{a} = \frac{N_{p} \left[ 2I_{a,1} - I_{a} \left( \frac{S_{a1} - S_{a1}}{\sqrt{3}} + S_{a2} \right) \right]}{N_{p} \left( \frac{S_{c1} - S_{p1}}{\sqrt{3}} + S_{a2} \right)}.$$
(13)

Note that  $I_a$  is replaced by  $I_{a,l}$ , where  $I_{a,l}$  is the fundamental rms component of  $I_a$ . Therefore, equation (13) describes the exact shape of  $I_a$  for a given load current  $I_a$ . Fig. 2 (a) shows the shape of  $I_a$  for sinusoidal input current. Fig. 2 (b) shows the input current waveform when the exact shape of current  $I_a$  is injected into the interphase reactor. From Fig. 2 (a), it is apparent that  $I_a$  is near triangular in shape. Simplifying the current  $I_a$  to a triangular wave shape and injecting it into the secondary winding of the interphase reactor yields a near sinusoidal input current  $I_a$ . Furthermore, generating a triangular injection current  $I_a$  into the secondary of the interphase reactor can be accomplished by means of a PWM-controlled current source as shown in Fig. 3.



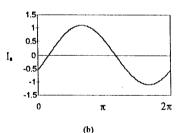


Fig. 2 (a) Injected current l<sub>x</sub> calculated from (13) (b) Input line current l<sub>x</sub> (pure sinusoidal)

## B. kVA rating of the injected current source, $l_x$

The line to line rms input voltage  $V_{LL}$  and dc output current  $I_d$  is assumed to be 1 per unit. The voltage across the interphase reactor  $V_m$  (see Fig. 1.) can be expressed as.

$$V_m = V_{d2} - V_{d1}. \tag{14}$$

$$V_{m} = -5.4018V_{LL} \sum_{n=1}^{\infty} \frac{1}{n^{2} - 1} \cos \frac{n\pi}{6} \sin \frac{n\pi}{12} \sin n(\omega t - \frac{\pi}{12})$$
 (15)

From (15), the rms value of V<sub>m</sub> can be computed as,

$$V_{m,max} = 0.1098V_{tL}$$
 (16)

The voltage across the interphase reactor secondary winding Vx is given by,

$$V_{x} = \frac{N_{x}}{N_{x}}V_{m}. \tag{17}$$

Then, from (16) and (17) the rms value of Vx is,

$$V_{s,max} = 0.1098 V_{LL} \frac{N_s}{N_s}$$
 (18)

From the results in the previous section, the peak value of the current  $I_{\rm g}$  is known to be 0.5  $I_{\rm d}$ . Therefore, the rms value of  $I_{\rm g}$  for a triangular waveshape is,

$$I_{1,m} = \frac{0.5}{\sqrt{3}} I_a \frac{N_p}{N_a} = 0.2887 I_a \frac{N_p}{N_a}$$
 (19)

The rms value of  $I_x$  can be reduced by adjusting turns ratio  $(N_y/N_s)$  between the primary and the secondary windings of the interphase reactor. From (18) and (19), the kVA rating of the injected current source, kVA<sub>RV</sub>, can be computed as,

$$kV\Lambda_{\text{DN}} = V_{\text{s,min}} \cdot I_{\text{s,min}}$$
$$= 0.0227 P_{\text{s}} (PU)$$
 (20)

Equation (20) shows that the kVA rating of the injected current source  $l_x$  is a small percentage of the output power. This demonstrates the superior features of the proposed scheme to realize a clean power utility interface.

## III. Implementation of the active current source I

Fig. 3 shows the circuit diagram for implementing the proposed scheme.
 Approaximated injected current I<sub>x</sub> is generated from a current controlled PWM inverter.

Block diagram of the gating signal generator for the PWM inverter is shown in Fig. 4. The reference for the injected current is synchronized with the input voltages. Injected current  $I_{\rm x}$  is fed back to the gating signal generator and the current error is compared to a triangular carrier wave (25k11z) to generate the PWM gating signals for the inverter switching devices.

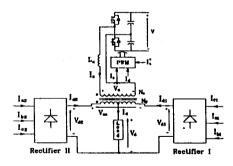


Fig. 3 Circuit diagram for implementation of the proposed scheme.

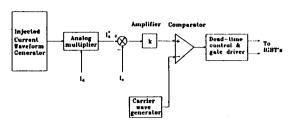
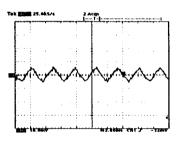


Fig. 4 Block diagram of the current-controlled PWM gating signal generator.

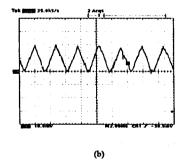
## IV. Experimental results

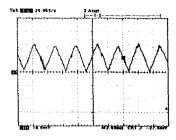
A 208V, 10kVA diode rectifier system employing the proposed scheme has been implemented in the laboratory. The interphase reactor employed in the experiment was designed with the turns ratio of  $N_p$ :  $N_s = 1:2$ .

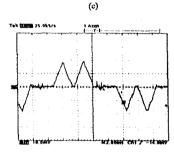
Fig. 5 shows the experimental results obtained. Fig. 5 (a) shows the injected current  $I_a$  and Fig. 5 (b) and (c) show the resulting rectifier output currents  $I_a$  and  $I_{d2}$ , respectively. Fig. 5 (d) shows the rectifier input current  $I_a$ . Finally, the experimental utility input current  $I_a$  and its frequency spectrum are shown in Fig. 5 (e) and (f), respectively. Note that input current  $I_a$  is near sinusoidal in shape and has less than 1% TIID. Experimental results show good agreement between theory and practice and demonstrate clean power characteristics of the proposed scheme.

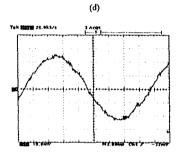


(a)









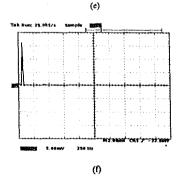


Fig. 5 Experimental results (5A/Div.)

(a) Injected current I<sub>x</sub> (b) Output current I<sub>d</sub> (c) Output current I<sub>d</sub> (d) Rectifier input current I<sub>d</sub> (e) Input line current I<sub>a</sub> (f) Frequency spectrum of I<sub>a</sub>

### V. Conclusion

In this paper a new active interphase reactor for a twelve-pulse rectifier system has been proposed. It has been shown that by injecting a low  $kV\Lambda$  (  $0.02\ P_{\bullet}$  (PU) ) active current source  $I_{\bullet}$  into the interphase reactor near sinusoidal input currents with tess than 1% THD can be obtained. The resultant system is a high performance clean power utility interface suitable for powering larger  $kV\Lambda$  ac motor drive systems. Detailed analysis of the proposed scheme along with design equations has been illustrated. Experimental results have been provided from a 208V, 10k VA rectifier system.

### References

- [1] "IEEE Recommended Practices and Requirements for Harmonic Control in Electric Power Systems", IEEE PES and Static Converter Committee of IAS, Jan. 1993
- [2] J. Schaefer, "Rectifier Circuits: Theory and Design", John Wiley & Sons, Inc., 1965
- [3] B. R. Pelly, "Thyristor Phase-Controlled Converters and Cycloconverters, John Wiley & Sons, 1971.
- [4] R. W. Lye, etc. al, " Power Converter Handbook", Power Delivery Department, Canadian General Electric Company Ltd., 1976
- [5] G. Seguier," Power Electronic Converters AC/DC conversions", McGraw-Ilill, New York, NY, 1986.
- [6] G. Oliver, etc. al, "Novel Transformer Connection to Improve Current Sharing on High Current DC Rectifiers", pp. 986-992, IEEE IAS conf., 1993.
- [7] S. Miyairi, etc. al, "New Method for Reducing Harmonics Involved in Input and Output of Rectifier with Interphase Transformer", IEEE Trans. on Industry Applications, pp. 790-797, Vol. IA-22, No. 5, Sep./Oct. 1986
- [8] A. R. Prasad, P. D. Ziogas, S. Manias, "An Active Power Factor Correction Technique for Three-Phase Diode Rectifiers", pp. 83-92, IEEE Trans. on Power Electronics, Vol. 6, No. 1, Jan. 1991
- [9] Ned Mohan, "A Novel Approach to Minimize Line-Current Harmonics in Interfacing Renewable Energy Sources with 3-Phase Utility Systems", IEEE APEC Annual meeting, 1992, pp.852-858.
- [10] S. Kim, P. Enjeti, P. Packebush and I. Pitel, "A New Approach to Improve Power Factor and Reduce Harmonics in a Three-Phase Diode Rectifier Type Utility Interface", pp. 1557-1564, IEEE Trans. on Industry Applications, Vol. 30, No. 6, Nov./Dec. 1994.
- [11] I. Pitel and S. N. Talukdar, "A Review of the Effects and Suppression of Power Converter Harmonics", IEEE IAS Conference, May 1977.
- [12] S. Choi, A. Jouanne, P. Enjeti, I. Pitel, "New Polyphase Transformer Arrangements with Reduced kVA Capacities for Harmonic Current Reduction in Rectifier Type Utility Interface", IEEE PESC conf., 1995
- [13] P. Enjeti and I. Pitel, "An Active Interphase Reactor for 12-pulse rectifiers", U.S. Patent disclosure