

Tunneling magnetoresistance of nano-scale magnetic tunnel junctions

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Recently, the magnetic tunnel junction (MTJ) has drawn much interest partly due to possible applications in magnetic random access memory (MRAM). The cell size is a critical issue for the high density MRAM, and active research has been conducted on junction patterning methods to reduce the cell size [1]. In this paper, we report on successful fabrication of nano-scale MTJs by the formation of insulating layers outside the junction area. The MTJs were grown by using DC magnetron sputtering, and the base pressure was below 1.0×10^{-7} Torr. The structure was Ta(50 Å)/NiFe(20 Å)/PtMn(150 Å)/CoFe(18 Å)/Ru(9.5 Å)/CoFe(28 Å)/AlO_x(10 Å)/CoFe(15 Å)/NiFe(30 Å)/Ta(100 Å)/Ru(100 Å), and patterning of 140 nm × 140 nm size junctions was carried out after annealing. When MTJs were patterned by dry etching using ion milling only, the tunneling magnetoresistance (TMR) was less than 5 % because of the damage to the junction edge during the process. In order to avoid this problem, we used the following recipe. The bottom electrode of MTJ was made by using (dry etching) ion-beam milling with a photo mask. After 140nm × 140nm memory cell was patterned by e-beam lithography, other parts except the junction was oxidized by ICP-RIE machine and passivated by AlO_x layer so that the top and bottom electrodes were not short-circuited. If the AlO_x layer is too thick, it is difficult to carry out the lift-off process with the photo resist (PR). Thus, the passivation of AlO_x layer was performed with two steps. Firstly, 10 nm thick AlO_x was grown on 140nm × 140nm ER and the junction was made by lift-off. Secondly, 50 nm thick AlO_x was grown on 10μm × 10μm PR and lift-off was done. Finally, 100 nm thick Al layer was grown for the top electrode. The MR curve is shown in Fig. 1 and the TMR was 31 %, which is much higher than that of MTJs of this size fabricated by a conventional method.

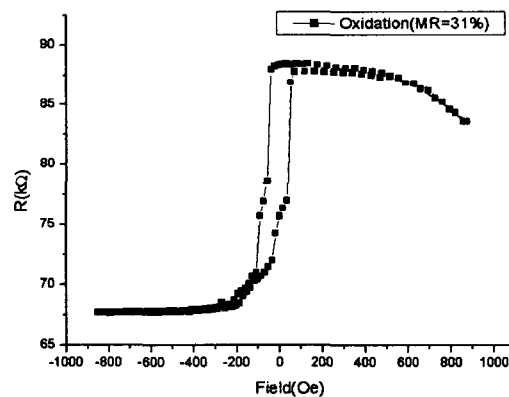


Fig. 1. MR curve for 140 nm × 140 nm MTJ

References

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