

RF Magnetron Sputtered Conducting $\text{La}_{1/2}\text{Sr}_{1/2}\text{CoO}_3$ Electrode on $\text{PbZr}_{0.4}\text{Ti}_{0.6}\text{O}_3$ Thin Film

Jai-Hyun Kim^{1*}, Hyouon Soo Kim¹, Woong Kil Choo¹, Bong Jin Kuh², Ki Vin Im² and Soon Ho Park²

¹ Department of Material Science and Engineering, Korea Advanced Institute of Science and Technology, 373-1 Gusong-Dong, Yusong-Gu, Taejeon, Korea,

² Process Development Team, Semiconductor R&D Center, Samsung electronics Company, San No. 24, Nongseo-Lee, Kiheung-Eup, Yongin-Si, Kyungki-Do, Korea

*Corresponding author: e-mail: s_k2@kaist.ac.kr, Phone: +82 42 869 4253, Fax: +82 42 869 4273

The best possible ferroelectrics which are used in non-volatile memory application may be either PZT or strontium bismuth tantalate ($\text{SrBi}_2\text{Ta}_2\text{O}_9$ or SBT).[1] In this study, we have investigated the ferroelectric and electrical properties of PZT 40/60 films grown on $\text{La}_{1/2}\text{Sr}_{1/2}\text{CoO}_3$ electrodes. Especially, the effect of the sputtered bottom LSCO electrode on the crystallographic orientation of the upper PZT ferroelectric films and the electrical properties or the capacitors will be described.

Bottom LSCO electrode films were deposited on the SiO_2/Si substrate by dc magnetron sputtering method. 150nm-200nm thin PZT 40/60 interlayer films were synthesized by spin coating of alkoxide sol-gel solutions.[2]

Conductive LSCO thin films can be obtained at relatively low temperature (at 450 °C) by magnetron sputtering of ceramic LSCO targets. From the XRD measurement, the electrode is shown to be in (100) film orientation.

With increasing the deposition temperature, however, the crystallographic orientation of PZT films changes from the (100) to a random orientation. From the I-V measurement, the leakage current is lowered with the deposition temperature while the polarization and fatigue shows only a small change. On annealing of PZT on LSCO, the leakage current beneficially goes down with annealing temperature below 650°C.

To investigate the LSCO top electrode effect, top LSCO electrodes have been deposited at RT and then annealed. A proper combination of adjusting the annealing temperature and time can reduce the resistivity of LSCO.

References

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