Structural and Electrical Properties of CulnSe₂ Ternary Compound Thin Film

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Abstract - Process variables for manufacturing the CulnSe₂ thin film were established in order to clarify optimum conditions for growth of the thin film depending upon process conditions (substrate temperature, sputtering pressure, DC/RF Power), and then by changing a number of vapor deposition conditions and Annealing conditions variously, structural and electrical characteristics were measured. Thereby, optimum process variables were derived.

For the manufacture of the CulnSe₂, Cu, In and Se were vapor-deposited in the named order. Among them, Cu and In were vapor-deposited by using the sputtering method in consideration of their adhesive force to the substrate, and the DC/RF power was controlled so that the composition of Cu and In might be 1 : 1, while the surface temperature having an effect on the quality of the thin film, was changed from 100°C to 300°C at intervals of 50°C.

1. Introduction

Looking into the global status of developing the solar cell of the Cu-II-V family, it can be known that the solar cell based on CulnSe₂ has demonstrated the highest efficiency so far, as shown in the references.

Studies on a tandem type of the ternary compound of the Cu-II-V family are little conducted. This study is to manufacture the CulnSe₂ compound thin film of n-type as a program for developing the leading technology for manufacturing a tandem type of the solar cell of the Cu-II-V family which enables a high efficiency of 22%[6] or higher to be accomplished by using only the ternary compound semiconductor thin film of the Cu-II-V family as manufactured by the SEL method which enables it to be manufactured at a low cost through the much simpler process than the process for manufacturing a quaternary or pentad compound to absorb a spectrum across the wide wavelength zone of the solar light in each unit lamination layer.

2. Experiment

In this experiment for manufacturing the CulnSe₂ ternary compound thin film, deposition was conducted by using the sputtering method and the evaporation method, and the Annealing was used in the electric furnace, and thereby, we intended to get a single-phase compound thin film. Thus, optimum process variables were derived by varying several deposition factors and heat treatment conditions differently and then measuring structural and electrical characteristics depending upon such variation.

In this experiment, Cu and In were deposited by using the sputtering method enabling us to expect the stronger adhesive force with a substrate, while Se was deposited by using the thermal evaporation method because its low meltingpoint prevents sputtering from being available. Since the sputterigrate and the electric conductivity of Cu is high, the DC sputtering method was used for deposition thereof. Meanwhile, for deposition of In, the RF sputtering method was used so that morestable sputtering might be available. Also, the sputtering rate depending upon the DC/RF power was controlled so that the composition ratio of Cu versus In might be around 1 : 1, and the substrate temperature affecting greatly the quality of the thin film was varied in the range of 100°C to 300°C at intervals of 50°C.

3. Results and Discussion

3.1 Crystallographic Characteristics of CulnSe₂

The diffraction fringe of X-ray, which depended upon the substrate temperature and the heat treatment temperature of the manufactured CulnSe₂ thin film, was investigated. Fig. 1 represents a case that a sample manufactured at the substrate temperature of 100°C was thermally treated at 200°C-350°C. Until the heat treatment temperature got to be 300°C, second-order phases of Cu₂Se, CuIn, F-CuSe and F-InSe appeared, and at the heat treatment temperature of 350°C, such second-order phases disappeared and only the single phase of CulnSe₂ was observed. At the low heat treatment temperature of 200°C, the phase of Cu₂In₃, an excess of Cu appeared, and at the heat treatment temperature of 250°C or higher, such phase, an excess of Cu disappeared, and second-order phases of F-InSe₃ and the like appeared. This variation is well consistent with the variation of the surface configuration at the heat treatment of 250°C. Such second-order phases did not appear at the heat treatment temperature of 350°C. In order to investigate this phenomenon more particularly, all of the samples as manufactured at different substrate temperatures were thermally treated at 350°C, and the result was shown in Fig. 2. As expected, second-order phases were not observed at all, and only single phases of CulnSe₂ appeared.
3.2 Electrical Characteristics of CuInSe₂

In order to examine the composition ratio of each element of CuInSe₂, the substrate temperature was varied in the range of room temperature to 200[°C] and the heat treatment temperature was varied in the range of 200[°C] to 350[°C] on the basis of the result of conducting the EDX analysis and the result of measuring the Hall effect to identify the conduction pattern, the carrier concentration and the Hall mobility thereof.

As a result, p-type and n-type appeared alternately. In order to analyze a cause of this result, an extent of an error beyond stoichiometry, which is represented by the value of δS, was obtained from the expression of \( δS = \left( \frac{12(Se) + (Cu+In)}{1.18} \right) \). It is known that if its value is (+), p-type is available, while if its value is (-), n-type is available. It can be known that this rule is well consistent with the experimental result showing n-type generally. Also, it can be known from the experimental result that if the composition ratio of each element is appropriately adjusted, p-type or n-type can be artificially acquired. The object of this experiment is to acquire CuInSe₂ of n-type. The carrier concentration and the Hall mobility thereof are generally lower than those of CuInSe₃ of p-type, but it is thought that there will be no difficulty in using it as an absorption layer. However, at the heat treatment temperature of 200[°C] or lower, no compound is formed at all. This has been ever identified also through the SEM photo. In order to examine a relation between the composition ratio thereof at the heat treatment temperature of 350[°C] or higher and the electrical characteristics thereof, the Hall effect was measured and the result was shown in Table I. Both samples have almost the stoichiometric composition. It can be known that as its composition gets to be closer to the stoichiometric composition, p-type is more strongly shown. It was also identified that in order to show n-type clearly in the condition of the heat treatment temperature of 350[°C] for 1 hour, the amount of In had to be more increased.

<table>
<thead>
<tr>
<th>Table 1</th>
<th>Best condition and result table of CuInSe₂ thin film fabrication</th>
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</table>

A.T- Annealing Temp, δS- Non-stoichiometry, CT- Conduction type, HM- Hall mobility [cm²/V·s], R- Resistivity [Ω·cm], Cc- Carrier concentration [cm⁻²]

4. Conclusion

In this experiment, the CuInSe₂ thin film was let to grow by using the sputtering method and the evaporation method to vary the substrate temperature on the glass substrate, the Annealing temperature and the composition ratio, and characteristics thereof were investigated. Asa result, the following conclusions could be drawn:

(1) Though it is known that in the case of CuInSe₂, intrinsically, a thin film of p-type is acquired with ease, while it is difficult to embody a thin film of n-type, in this experiment, the CuInSe₂ thin film of n-type was embodied with ease.

(2) In the CuInSe₂ thin film, phase transition took place in the vicinity of 250[°C], and a thin film of a single phase was acquired at the heat treatment temperature of 350[°C].

(3) The carrier concentration, the Hall mobility and the resistivity of the CuInSe₂ thin film as acquired through the experiment were 1.27 × 10⁶[Ω·cm], 4295 [cm²/V·s] and 10⁻⁵[Ω·cm], respectively. In the light of the above results, it is thought that the CuInSe₂ thin film as acquired in this experiment has physical properties suitable for embodying the solar cell.