PP-02

Fabrication and characterization of the ZnO based thin film transistors

Eun-Jeong Yang, Jae-Hong Lim, Dae-Kue Hwang, Chang-Hee Cho, Min-Suk Oh, Chang-Goo Kang and Seong-Ju Park*

Nanophotonic Semiconductors Laboratory, Department of Materials Science and Engineering, Gwangju Institute of Science and Technology, Gwangju 500-712, Korea

ZnO has much interest as a potential material for application in TFT due to many advantages, such as, high transmittance in the visible region, wide band gap, high thermal stability, robustness to light radiation and easy processing.

We report on the fabrication and characterization of ZnO based thin film transistor. ZnO was deposited as a channel layer at $300\,^{\circ}$ C by radio frequency magnetron sputtering in Ar/O₂ mixture. SiN_X and indium tin oxide (ITO) were deposited on glass substrate as a gate insulator and gate electrode, respectively. We deposited ZnO thin films at different rf power as a channel layer. The SEM image and XRD analysis showed that a ZnO thin film deposited at rf power of 80w has a good crystal quality. This film showed the best saturation current and the field effect mobility. The ZnO TFT showed a mobility of $19\text{cm}^2/\text{Vs}$, on/off ratio of 10^4 , threshold voltage of 15V and subthreshold voltage of 5.01V/decade.