Flyback-type Snubber of High Efficiency for 10kV IGCT in 7MW Wind Turbine Systems

Siamak Shirmohammadi and Yongsug Suh

Department of Electrical Engineering, Smart Grid Research Center, Chonbuk National University

ABSTRACT

10kV IGCT has been recently developed and has the potential to push wind turbine systems to higher power and voltage rating. Converters employing IGCTs need snubber and OVP circuit to limit the rate of rise of current and peak over voltage across IGCT during turn on and off state respectively. The conventional RCD snubber which is used in such power converter dissipates a significant amount of power. In order to reduce the amount of energy lost by conventional RCD snubber, this paper proposes an isolated inductor snubber circuit that not only meets all of the IGCTs characteristics during on and off-state but also significantly saves the power loss. Loss analysis of conventional di/dt snubber and OVP circuit is performed for the 3-level NPC type back-to-back VSC supplied from grid voltage of 6.9kV. In comparison with the conventional snubber, isolated inductor snubber has a fewer number of components and improved efficiency leading to a reliable and efficient wind turbine systems.

1. INTRODUCTION

During the last years, the installed wind power capacity has increased significantly, and an interesting area for further development is offshore wind farms. The back-to-back connected power electronic converters in medium-voltage (MV) level are generally realized as multi-level (ML) voltage source converters (VSC) instead of 2L-VSCs in order to improve the performance factors regarding switch power losses, harmonic distortion, and common mode voltage/current [1]. As the most commercialized ML converter, PMSG (Permanent Magnet Synchronous Generator) wind turbine system with a back-to-back 3L-NPC VSC is presented in Fig. 1 [2], [3].

Power semiconductors have experienced a rapid technical improvement toward higher blocking voltages and current ratings, more reliable packages, and extended safe operating areas (SOAs) [4]. Today 3.3, 4.5, and 6.5kV IGBTs (modules or press-packs) and 4.5, 5.5 and 6.5kV IGCTs (press-pack only) presented in Fig. 2 are applied in newly developed industrial 3L-NPC VSCs. Converters employing IGCTs require a small clamp inductor which limits shortcircuit peak currents and the rate of rise of current, di/dt, during IGCT turn-on transients to stabilize diode turn-off transients within the SOA, as well as homogenous IGCT turn-on transients.



Fig. 1. 3L-NPC back-to-back configuration for multi-MW PMSG MV wind turbines



Fig. 2. Press-pack type IGCT (left) and Diode (right) for MV wind turbines

The integration of one 10kV IGCT and one fast 10kV diode is a very attractive solution for converters since the expense for the mechanical construction and cooling can be substantially reduced. This medium voltage drive employing 10kV IGCTs needs a di/dt limiting inductor to meet the required di/dt characteristics during

switching on transients. This di/dt limiting inductor usually necessitates an additional over voltage protection (OVP) snubber or clamp circuitry as shown in Fig. 1. Thus this snubber circuitry dissipates additional power loss and gives a rise to an important loss factor. There have been several kinds of active RCD snubber for GTO device trying to meet both wide Safe Operating Area (SOA) and low loss [5]-[9]. However those snubber circuitries add device count and circuit complexity.

In this paper a new flyback-type di/dt snubber circuit which is simpler and more efficient than conventional RCD snubber for 10kV IGCT in MV wind turbines is proposed. This flyback-type di/dt snubber adopts a flyback type transformer. This flyback-type di/dt snubber circuit is very effective in the restriction of the di/dt characteristics. In addition, the power loss caused by OVP circuit is reduced.

2. CONVENTIONAL SNUBBER CIRCUIT FOR IGCT

It is shown that the use of 10kV IGCTs enables a reduction of the total number of the main power components by 41 - 71% compared to a series connection of 4.5kV or 5.5kV IGCT devices. Major operating characteristics of 10kV IGCT and diode are summarized in Table 1.

Table 1. Characteristics of 10kV Press-pack type IGCT and Diode for 7MW MV 3L-NPC VSCs [10], [11]											
DEVICE	BLOCKING Voltage	I _{тбQM} / I _{F(AV)M}	V то (Max.)	R т (Max.)	E on (Max.)	E off (Max.)	Meas. condition	T _{vj_max}	R _{th(j-c)}	R _{th(c-h)}	R _{th(h-a)}
IGCT	10 kV	2000A	2.7V	1.2mΩ	3.4J	34 J	6kV/2000A	125℃	8.5K/kW	3K/kW	6K/kW

DIODE 10 kV 1700A 2.7V 2.7mΩ - 17.6 J 6kV/1700A 125℃ 6K/kW 3K/kW 6K/kW

Converters employing IGCTs need a di/dt limiting inductance to meet the required di/dt characteristics during switching on transients. This di/dt limiting inductor (Li) usually necessitates an additional over voltage protection snubber or clamp circuitry as shown in Fig. 3. It is required to clamp the over-voltage spike generated during the turn-off transient by a clamp capacitor (C_{Cl}). The di/dt limiting inductor (Li) stores magnetic energy when the IGCT is conducting and this energy needs to be dissipated somewhere, otherwise it will oscillate between C_{Cl} and Li. Therefore, a clamp resistor (Rcl) is also required to dissipate this energy. The clamp diode (D_{cl}) prohibits the snubber capacitor (C_{cl}) from discharging through IGCT. The inclusion of the clamp circuit increases the component count per building block, which is considered to be one of the disadvantages of using IGCT.



Fig. 3. di/dt snubber and OVP clamp circuit for IGCT in the upper-half part of 3L-NPC VSCs

The snubber circuitry dissipates additional power loss and gives a rise to an important loss factor. At the instant of switching off, the stored magnetic energy from the on-state current of the IGCT is given by;

$$E_{L_i} = \frac{1}{2} L_i \cdot i(t)^2$$
 (1)

This stored energy is mainly dissipated in the snubber resistor (R_{Cl}) or fed back to charge the dc link capacitor (C_{DC-link}). In this paper, the total stored energy in the di/dt limiting inductor is assumed to be snubber circuit power loss. Therefore, snubber circuit power loss

(P_{Cl}) can be expressed as;

$$P_{cl} = \left(\sum_{k=1}^{n} E_{L_{i}}\right) \times f_{sw} = \left\{\frac{1}{2}L_{i}\sum_{k=1}^{n}i_{k}(t)^{2}\right\} \times f_{sw}$$
(2)

3. FLYBACK-TYPE SNUBBER CIRCUIT FOR IGCT

The proposed flyback-type di/dt snubber configuration is shown in Fig. 4 (a). Single-phase leg circuit in the upper-half part of 3-level NPC converter is considered for the sake of simple analysis.



Fig. 4. Flyback-type snubber circuit for IGCT in the upper-half part of 3L-NPC VSCs

Figure 5 illustrates the current flow path for each operating mode of flyback-type di/dt snubber circuit. There are three operating modes during one switching period of single IGCT in Fig. 4.



(a) Current flow in Mode I (b) Current flow in Mode II (c) Current flow in Mode III Fig. 5. Operation of flyback-type di/dt snubber for IGCT in the upper-half part of 3L-NPC VSCs

The voltage at the primary side of flyback-type transformer is obtained as;

$$V_{flyback TR} = \frac{n_p \times \frac{1}{2} V_{DC-link}}{n}$$
(3)

The peak voltage across IGCT becomes as the following.

$$V_{IGCT} = \frac{V_{DC-link}(n_p + n_s)}{2n}$$
(4)

4. SIMULATION RESULTS OF SNUBBER CIRCUIT FOR 10KV IGCT

The operation of proposed flyback-type di/dt snubber circuit is verified through the simulation using Plecs tool. Detailed simulation condition for the proposed circuit in Fig. 4 is summarized in Table 2.

Table 2.	Simulation	Parameters	of Fl	yback-typ	be di/dt	Snubber	Circuit

Parameter	Symbol	Value
DC-link voltage	$V_{DC-link}$	11 2 kV
Switching frequency	fgsc_pwm	1020 Hz
Magnetizing inductance	L_m	13 6 uH
Turn ratio (Primary winding/ Secondary winding)	$n (=n_p/n_s)$	1/8

The waveforms of voltage and current of IGCT during the IGCT's switching transient process are shown in Fig. 6. The voltage overshoot peak (VDSP) of IGCT can be adjusted by selecting the proper turn ratio of flyback-type transformer. The waveforms of voltage and current of clamp diode during switching transients are given in Fig. 7.





clamp diode during each operating mode

In this paper, the loss analysis is performed to compare the loss factors due to the proposed and conventional snubber circuits. The simulation is performed based on the parameters of 7MW MV 3L-NPC VSCs as specified in Table 3.

Table 3. System Specifications of 7MW MV 3L-NPC VSCs	Table 3. S	vstem Spec	ifications of	of 7MW I	MV 3	L-NPC VSCs	
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Parameter	Symbol	Value	Per unit	
Output power	Prated-out	7 MW	10	
Grid frequency	fgrid	60 Hz	10	
Grid side inductance	Lgrid	2 5 mH	017	
Grid side input voltage	VII	69 kV	10	



Fig. 8. Waveforms of switching and current in each phase-leg of 10kV IGCTs (pf=0 9)



Fig. 11. Total loss distribution of each power semiconductor device (pf=0 9 leading, three legs)

5. CONCLUSION

In this paper flyback-type di/dt snubber circuit for 7MW PMSG wind turbine employing a 3L-NPC back-to-back voltage source converter is presented. As compared to the conventional snubber circuitry for IGCT-based converters, the proposed flyback-type di/dt snubber circuit using flyback-type transformer has such good features as a fewer number of snubber component and improved efficiency. The 10kV IGCT for the line voltage classes of 6 - 7.2kV MV 3L-NPC VSCs is investigated and basic operation of the flyback-type di/dt snubber circuit is discussed. The switching frequency is set to 1020Hz under the grid side input voltage of 6.9kV. The turn ratio of flyback-type transformer in the proposed concept plays a critical role in the performance of snubber circuit and the proper selection of clamp diode. The proposed flyback-type di/dt snubber can save the loss (34kW, 0.49%) of conventional snubber circuit in 7MW back-to-back 3L-NPC VSCs at most without considering the core losses newly incurred by the flybacktype di/dt snubber.

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