# Implementation of SoC for NMEA2000 Ship Standard Network Protocol Using FPGA

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**Abstract**: IEC61162-3 known as NMEA2000 protocol is approved as a standard network of SOLAS ship by ISO and used for the instrument network which exchanges data in real-time. For easy the development of ship network equipments, this study is focused on the development of SoC which can convert to NMEA2000 protocol from various kind of protocols such as TCP/IP, NMEA0183, RS422 and others using FPGA and u-Blaze. In this paper, we composed NMEA2000 protocol stack on FPGA and verified NMEA2000 network communication of FPGA system by connecting with commercialized devices through PC Hyper-terminal and network monitoring program.

Key words: NMEA2000, Ship Network, u-Blaze, FPGA, SoC

### 1. Introduction

In Korea, the market of ship building has been grown as the world economy progresses. However, our ship material industry has not been significantly increased, it mostly depends on import. IMO(International Maritime Organization) decided to take e-navigation for ship security and safety. E-navigation needs new integrated ship material system, but our ship material industry is not even ready to apply e-navigation. In this situation. if e-navigation start in 2012[1-2], Korea will not be available to keep prior occupation of world ship market. Therefore, it is important to secure the core technology of ship electronics and IT convergence material. Realization of e-navigation is embodied by two stages. One is standardization and integration of existing technologies, the other is the implementation and the development of new technologies. Especially, in case of ship network, many kinds of network are used in the ship. Because of using various ship networks, it is possible to occur compatible problems among devices or obstruction of safety voyage. Consequently, the adaption standard of ship network is necessary to integrate present networks. This standard of ship network is largely divided into two parts. Firstly, NMEA(National Marine Electronic Association)2000 protocol known as IEC (International Electro-technology Commission) 61162-3 is used in instrument network

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which exchanges data in real-time[1,3]. The other, MiTS(Maritime information Technology Standards) defined as IEC 61162-4 is used in control network. NMEA2000 protocol and MiTS are already approved in ISO(International Organization for Standardization)[4].

Purpose of this research is the implementation of NMEA2000 protocol using FPGA, also, it is helpful to easily develope ship IT equipments connected to NMEA2000 network by supply SoC. This paper focuses on the verification of NMEA2000 protocol stack using FPGA and u- Blaze, and the composition of system for test of communication and compatibility between standard networks.

## 2. Protocol architecture 2.1 NMEA2000 protocol

Since 1994. NMEA2000 has been performed by leading of the NMEA committee which co-worked with the United States Coast Guard, universities, many navigation & communication equipment manufacturers and CAN solution companies. The first specification of NMEA2000 was completed in 2001[5]. NMEA2000 is real-time communication network of SOLAS(international convention for the Safety Of Life At Sea) ship which interconnects marine electronics units as low-cost, 250kbps speed, bi-directional communication and multiple transmitting/ eceiving.

## 2.2 Structure of NMEA2000 protocol

NMEA2000 protocol uses 29bits identifier based on CAN 2.0B for communication and discernment between devices. Identifier field of NMEA protocol is composed with 3bits priority, 18bits PGN (Parameter Group Number) and 8bits SA(Source Address)[6]. The major component in identifier is PGN, because PGN includes the information of various ship devices. Also, each device can be distinguished from other device by PGN. SA,\_network node number, can be changed value by priority and name field value of device. Table 1 shows CAN 2.0B message frame.

Table 1: CAN 2.0B message frame

CAN 2.0B Message Frame													
	Arbitration field (32bits)				Control Field (6bits)		1	Data field (64bits)	CRC Field (16bits)				
S OF	I DE NTIFIER	S R R	I D E	nxheizo-oz -deizh	R T R	RESERVED	RESERVED	D L C	Data field	C R C	CRC DELIMITER	A C K N O W L E D G E	H O H
1	11	1	1	18	1	1	1	4	64	15	1	2	7

Table 2: The number of available PGN

		PGN						
PDU Format	Priority 3bits	EDP 1bit	DP 1bits P	PF 8bits GN	PS 8bits	SA 8bits	Available Number	
PDU1	0-7	0	0	0-239	DA 0-255	SA 0-255	240	
PDUI			1	0-239	DA 0-255	SA 0-255	240	
	0-7	0	0	240-2	GE	SA	16x256	
PDU2				55	0-255	0-255	=4096	
PD02			1	240-2	GE	SA	16x256	
				55	0-255	0-255	=4096	

PGN of NMEA protocol is composed with EDP(Extended Data Page), DP(Data Page), PF(PGN Format), PS(PGN Specific). According to value of PF, PDU is largely divided into PDU1 and PDU2. In case of PDU1, the number of available PGN is 480 because PF has unique value in this range. When PF has value between 240 and 255, PS expresses GE(General Extension). Therefore, the number of available PGN in PDU2 is 8,192. Table 2 shows the number of available PGN each case.

# 3. Composition of system 3.1 Structure of composed system

In this research, FPGA based u-Blaze was used for testing NMEA2000 protocol. FPGA based u-Blaze is programmable logic elements and interconnects in semiconductor devices. FPGA based u-Blaze can have shorter development time and better error correction than conventional u-processor board. u-Blaze which was soft processor core was used to control memory and logic elements. Also, Virtex-4<sup>TM</sup> ML401 XC4VLX25 was used as evaluation board to make test bed for trial SoC[7-8], CAN IP(Intellectual Property) was mounted on FPGA for CAN controller to communicate with CAN network, and UART IP was mounted for data monitoring through PC Hyperterminal[9]. Figure 1 shows structure of composed system bus with IP.

In order to communicate with CAN network, CAN clock must be  $8 \sim 24$  MHz, but system clock in ML401 is 100MHz. Therefore, clock value in MHS (Microprocessor Hardware Specification) was set to 20MHz for matching CAN clock port using lock generator function. For transmitting and receiving CAN signal, extension port in ML401 was set up to transmit and receive each pin[7–9]. However, there is no CAN driver in ML401. The voltage level is different between ML401(3.3 or 5V) and CAN network(12V). Therefore, in order to solve these problems, MCP2551 as CAN transceiver was mounted on previous extension port such as Figure 2. CAN high and low signal on MCP2551 are connected to NMEA2000 protocol stack on FPGA, and it can abstract NMEA2000 data from signal. Figure 3 shows whole composed system with FPGA and IP.

PLL	Bus Interface	s Ports	Addr	resses		
	Name	Bus Name		IP Type	IP Version	IP Classification
	■ microblaze_0			🛨 microblaze	7,20,a	Processor
	- DLMB	dimb	~			
<u> </u>	- ILMB	ilmb	¥			
	- DPLB	mb_plb	~			
	- IPLB	mb_plb	*			
T I I	- DXCL	microblaze_	.0			
	- IXCL	microblaze_	0_ ···			
	DEBUG	microblaze	··· 🖌			
	- TRACE	microblaze_				
	dimb			👷 lmb_v10	1,00,a	LMB Bus
	- ilmb			🙀 lmb_v10	1,00,a	LMB Bus
	mb_,plb			☆ plb_v46	1,04,a	PLBV46 Bus
	dlmb_cntlr			🐈 lmb_bram…	2,10,b	Memory Controller
0 0	- SLMB	dimb	~			
K	BRAM	dimb_cntir_				
	∃ ilmb_cntlr			🐈 lmb_bram…	2,10,6	Memory Controller
<b>b</b>	- SLMB	ilmb	<b>v</b>			
<hr/>	- BRAM				F 00 -	Marrie Castella
	DDR_SDRAM			👷 mprnc	5,00,a	Memory Controller
•	SPLB0	mb_plb	*	A constants	2.00 -	Memory Controller
	SRAM	mb_plb	~	☆ xps_mch	5,00,a	Memory Controller
•	- SPLD	nio_pio	v	🐈 bram_block	1.00 a	Memory
	- PORTA	ilmb_port	~	T DIaIII_DIOCK	1,00,a	Memory
	- PORTB	dimb_port	v			
	= mdm_0	unipepon		🖕 mdm	1.00.e	Debug
	- SPLB	mb_plb	v	M HIGHT	1,00,0	00003
<b>4</b>	- MFSL0	No Connec				
	- MBDE	mdm_0_MB				
` I	- XMTC	mdm_0_XM	TC			
	Ethernet			👉 xps_ether…	2,01,a	Peripheral
	SPLB	mb_plb	*			
Ĭ	DIP_Switc…			🆕 xps_gpio	2,00,a	Peripheral
	- SPLB	mb_plb	Y			
Ī	😑 LEDs_4Bit			🐈 xps_gpio	2,00,a	Peripheral
	- SPLB	mb_plb	~			
	LEDs_Posi			🚖 xps_gpio	2,00,a	Peripheral
	- SPLB	mb_plb	~			
	Push_Butt			🐈 xps_gpio	2,00,a	Peripheral
	- SPLB	mb_plb	*			
	IIC_EEPROM SPLB	ask alk	1272	🐈 xps_iic	2,01,a	Peripheral
	= xps_intc_0	mb_plb	*	Access late	2.00.a	Interrupt Controller
	SPLB	mb_plb	~	🐈 xps_intc	2,00,a	memory Controller
9	SVSACE	mo-pip	×	🐈 xps_sysace	1.01.a	Peripheral
	SPLB	mb_plb	¥	M ubershare	1,01,0	r enprietdi
<b>9</b>	= AS232_Uart	mo-più	· ·	🖕 xps_uartlite	1.01.a	Peripheral
	SPLB	mb_plb	~	- apollograne	.,	. Suprora
	- clock_gen…		udi	🐈 clock_gen…	3.00.a	IP
	proc_sys			proc_sys		Peripheral

Figure 1: Structure of composed system bus with IP

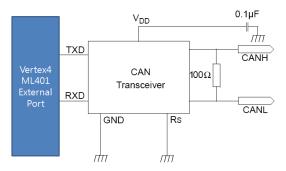


Figure 2: Schematic diagram of CAN transceiver with ML401

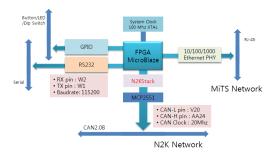


Figure 3: Block diagram of mounted u-blaze and IP on Virtex4 FPGA

#### 3.2 NMEA2000 protocol stack

For NMEA2000 network communication, equipment needs to own SA. SA must be unique. If same SA exist on network, overlapped network devices have to adjust own SA. For this work, NMEA2000 protocol uses PGN59904 and PGN60928. After POST(Power On Self Test), each equipment broadcasts PGN59904 on network, including device information and SA. During the broadcast, all network devices response to this request. Due to this task, each device can compare with other name filed value and priority. This task is called as address claim. When address claim occurs, network device should response within 250ms bv PGN60928. If any devices do not response this time, they could ignore. In case of same SA, appropriate network device compares the priority of each device. If priority is same, it compares name field value. If name field value is higher than SA other. will be changed. This information is included in PGN60928.

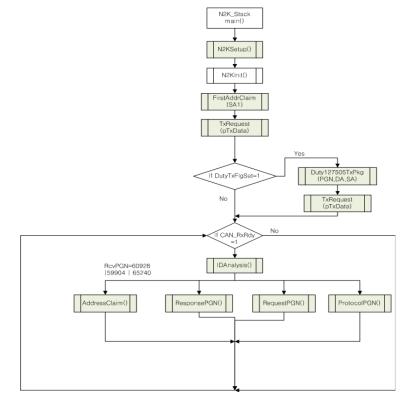


Figure 4: Main routine in NMEA2000 stack

Also, network device which changed SA runs address claim again. If each network device has own SA by this task, each device could send periodic data PGN, or request and response PGN to other device on the network. The major part of NMEA2000 stack is as follows Figure 4.

## 4. Experiment

### 4.1 Creation of PGN data

For verification of implemented NMEA2000 protocol stack. network communication connection and data are required. First of all. we mounted NMEA2000 protocol stack ML401 and coded PGN127505 on including fluid level data[6.10] in periodic function. Second name field in PGN127505 includes fluid type based on each case. Table 3 appears hexadecimal value depending on fluid type. We chose three data types such as Fuel(0x00), Fresh Water(0x01), and Oil(0x04), ML401 could generate dummy random data about these types of fluid.

Hexadecimal value	Fluid type
0x00	Fuel
0x01	Fresh Water
0x02	Waste Water
0x03	Live Well
0x04	Oil
0x05	Black Water(Sewage)
$0x06 \sim 0x0D$	Reserved
0x0E	Error
0x0F	Data not available

Table 3: Hexadecimal value depending on fluid type

#### 4.2 Configuration of communication test

Actual configured ML401 was connected with already qualified equipments such as anemometer of Maratron, name of manufacture and fuel flow sensor, temperature sensor and speed sensor of Lowrance manufacture. This network device occurs a lot of data. Network device should use acceptance filter before overflowing unnecessary information. ML401 was configured acceptance filter for receiving specified PGN values(59904. 60928 126464, 126996 and 126998 in decimal). These PGN values are related to address claim and request other device response except periodic general data. Also, baud rate and pre-scaler were set to 250Kbps CAN network was basically used. as Figure 5 shows composed daughter board with CAN transceiver to connect CAN network. Figure 6 shows composed NMEA2000 network equipments for communication test.



Figure 5: Composed CAN transceiver board with ML401



Figure 6: Composed experiment equipments of NMEA2000 network

#### 4.3 Communication test using PC Hyper-terminal

For verification of data communication between ML401 and network, we used UART port on ML401 and PC Hyperterminal. Because of acceptance filter in FPGA, ML401 only received the permitted PGN such as 59904, 60928 and others. Figure 7 shows data field values whenever PGN value is 59904 or 60928. These PGN means address claim on network works well and ML401 has own unique SA.

test - \$000000	
3월(E) 편집(E) 보기(Y) 호송(C) 전송(D) 도움왕(H)	
) 📽 🐵 🌋 📣 🎦 🕼	
	3
Tx_DATA0 : C77DFE3C	
TX DATA1 : 80000000	
Tx_DATA2 : 0369212B	
Tx_DATA3 : 00826400	
Rx_DATA0 : 00000000	
Rx_DATA1 : 00000000	
Rx_DATA2 : 00000000	
Rx_DATA3 : 00000000	
CAN_RecvDATA : 18EA0000	
Mask_RecvPGN : 0×EA00 ID : 18EA0000	
RcvNsg.CMDPGN : 0x00EE0000	
1. RecvPGN : 0000EA00, 59904, 3	
TX_DATA0 : C77DFE3C	
Tx_DATA1 : 80000000 Tx_DATA2 : 03692128	
TX_DATA2 - 03692128 TX_DATA3 : 00826400	
TX_DATA3 : 00020400 Rx_DATA0 : C75C0000	
R× DATA1 : 30000000	
R× DATA2 : 00EE0000	
Rx DATA3 : FF00FFFF	
CAN RecvDATA : 18EEFF00	
Mask RecvPGN : 0×EE00 ID : 18EEFF00	
2. RecvPGN : 0000EE00, 60928, 8	
	8
결 254:02 · 자동 검색   115200 8-N-1   SCROLL   CAPS   NUM   접   에크	

Figure 7: Communication between composed NMEA2000 network equipments to PC

#### 4.4 Network monitoring and data verification

For data verification, we made network monitoring program. This program shows device status including SA. network connection status, manufacture code. device class, identity value and others on addition. real-time. In we used CAN-to-USB converter to check real data from physical layer. Figure 8 shows actual network monitoring application and real network data. This picture confirms that the SA of ML401 is 30. Also, its identity is 92419. Indeed, according to manufacture code list from NMEA, manufacture value of ML401 is 345 defined as AITASC (Advanced IT And Ship Convergence center). This figure also shows other

devices such as equipments of Lowance and Maratron.

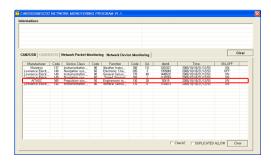


Figure 8: NMEA2000 protocol monitoring program

For verification real periodic data, we made monitoring application. This application can display information of connected device in NMEA2000 network. Figure 9 shows navigation equipments data from Lowrance and Maratron. Figure 10 shows dummy data which comes from composed ML401 including level of fuel, fresh water and oil.



Figure 9: Navigation data



Figure 10: Engine and tank data

## 5. Conclusion

In this study. Tank level device was implemented with proposed NMEA 2000 protocol stack SoC which consists of CAN, UART and other peripherals including IP component and u-Blaze from Xilinx on existing NMEA2000 network to be composed of certified equipments. After address claim, composed ML401 with proposed NMEA 2000 protocol stack SoC sent periodic data PGN of fluid level data depending on each type. Through this experiment, possibility of SoC implementation was confirmed, and NMEA2000 stack was verified. In the future, USB, TCP/IP and others will be ported in FPGA as proposed method to make dedicated NMEA 2000 protocol stack SoC. Then, new integration system which can convert various communication protocol to NMEA2000 protocol will be developed.

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