

An Analytical Model for Deriving the 3-D Potentials and the Front and Back Gate Threshold Voltages of a Mesa-Isolated Small Geometry Fully Depleted SOI MOSFET

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Abstract—For a mesa-isolated small geometry SOI MOSFET, the potentials in the silicon film, front, back, and side-wall oxide layers can be derived three-dimensionally. Using Taylor's series expansions of the trigonometric functions, the derived potentials are written in terms of the natural length that can be determined by using the derived formula. From the derived 3-D potentials, the minimum values of the front and the back surface potentials are derived and used to obtain the closed-form expressions for the front and back gate threshold voltages as functions of various device parameters and applied bias voltages. Obtained results can be found to explain the drain-induced threshold voltage roll-off and the narrow width effect of a fully depleted small geometry SOI MOSFET in a unified manner.

Index Terms—3-D analytical SOI MOSFET model, threshold voltage roll-off, short channel effect, narrow width effect, natural length

I. INTRODUCTION

Since silicon-on-insulator (SOI) MOSFETs are inherently possessed of several superior electrical characteristics over bulk-type MOSFETs, such as better isolation between source (drain) and substrate, punch through suppression, enhanced current driving capability,

and suppressed short channel effects [1-6], they are nowadays facilitating the integration level of VLSI up to ULSI. In order to achieve extremely high integration in ULSI, the geometry of a SOI MOSFET is very small by reducing the channel (gate) width as well as the channel length. Then for a mesa-isolated small geometry fully depleted (FD) SOI MOSFET, the potential in the silicon film (body) becomes strongly coupled with the potentials in the front, back, and side-wall oxide layers. Thus, in order to have accurate and rigorous modeling the characteristics of a mesa-isolated small geometry SOI MOSFET, three-dimensional analysis should be required. Especially, the narrow width effect and the short channel effect will offer combined influence on the threshold voltage. The threshold voltage is very crucial parameter since it can estimate several features of a MOSFET such as channel conductance, drain saturation voltage/current, turn-on/off speed, etc. Recently, by extending the two-dimensional (2-D) analytical models [7-13] up to three-dimensional (3-D) analysis, 3-D models [14-17] for the threshold voltage have been suggested. However, employed 2-D analytical models were somewhat oversimplified in the sense that the potentials in both front and back oxide layers were derived by using the gradual channel approximation (GCA), the potential in the silicon body was derived by decomposing it into a 1-D Poisson's equation and a 2-D Laplace's equation. In solving the 2-D Laplace's equation, rather complicated infinite series expansions have been required to satisfy the suggested equi-potential boundary conditions at both planes of source-body and drain-body $n^+ - p$ junctions. Hence, the front and back surface potentials have been

expressed as certain infinite series expansions, which have been approximated by taking only one term in the derivation of the minimum front and back surface potentials. These approximations seem to be somewhat oversimplified and inconsistent with the suggested equipotential boundary conditions. In the 3-D models [14, 17], complicated double summations of infinite series expansions have been used for deriving the 3-D potential in the silicon body. However, since the approximation of the double summations of infinite series by taking only one term seems to be rather oversimplified and the analytical logic of the above 3-D models will be collapsed in case that one of oxide layers is fairly thick, the derived expressions for the front and back gate threshold voltages seem to be more or less ambiguous.

In this paper, starting from the potential expressions obtained by using the GCA and updating them iteratively by taking into account their y - and z -variations, we can derive the potentials in the silicon body, front, back, and side-wall oxide layers three-dimensionally. From the derived 3-D potentials, the minimum values of the front and back surface potentials can be derived and used to obtain the closed-form expressions for the front and back gate threshold voltages as functions of various device parameters and applied bias voltages.

This paper is organized as follows. In Section II, the basic model formulation is described. In Section III, some computation results and discussion are contained. A summary and a conclusion are in Section IV.

II. MODEL FORMULATION

Schematic cross section views of a mesa-isolated fully depleted SOI MOSFET are shown in Figs. 1(a) and 1(b). Here, x is the channel-depth directed coordinate from the front oxide/silicon interface toward the silicon body; y is the longitudinal coordinate from the source side end toward the drain; z is the transversal coordinate from the middle of the silicon film toward one side end; L and w are the channel length and width, respectively; t_{si} is the silicon film thickness; t_{of} , t_{ob} , and t_{ow} are the thicknesses of the front, back, and side-wall oxide layers, respectively; V_{GS} , V_{BS} , and V_{DS} are front gate, back gate, and drain voltages, respectively. In order to establish an accurate and rigorous model for the front and back threshold voltages of a mesa-isolated

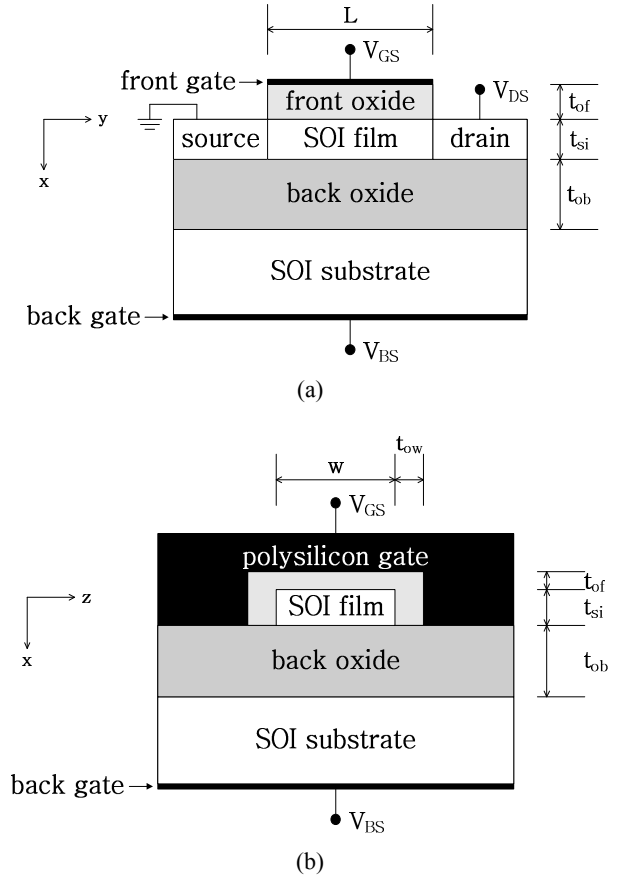


Fig. 1. (a), (b) Schematic cross-section views of a mesa-isolated n-channel SOI MOSFET to be modeled.

small geometry SOI MOSFET, let us assume that the device structure is symmetric about the axis of $z = 0$, the space charge densities in the oxide layers are negligible, and the silicon body is uniformly p-doped and fairly thin so that the silicon body is fully depleted in the sub-threshold regime. Then we can consider the following 3-D Laplace/Poisson's equation:

$$\frac{\partial^2 \psi}{\partial x^2} + \frac{\partial^2 \psi}{\partial y^2} + \frac{\partial^2 \psi}{\partial z^2} = \begin{cases} 0, & \text{in the oxide layers,} \\ \frac{qN_A}{\epsilon_{si}}, & \text{in the silicon body,} \end{cases} \quad (1)$$

where $\psi(x, y, z)$ is the 3-D electrostatic potential, q is the elementary charge, N_A is the uniform acceptor density in the silicon body, and ϵ_{si} is the dielectric constant of silicon. For convenience, we separately denote the potentials in the silicon film, front, back, and side-wall oxide layers as $\psi^{si}(x, y, z)$, $\psi^{of}(x, y, z)$, $\psi^{ob}(x, y, z)$, and $\psi^{ow}(x, y, z)$, respectively. Boundary conditions subject to Eq. (1) will be as follows:

$$\psi^{of}(-t_{of}, y, z) = V_{GS} - \phi_{msf} = V'_{GS}, \quad (2a)$$

$$\psi^{ob}(t_{si} + t_{ob}, y, z) = V'_{BS} - \phi_{msb} = V'_{BS}, \quad (2b)$$

$$\psi^{ow}(x, y, \pm w/2 \pm t_{ow}) = V_{GS} - \phi_{msf} = V'_{GS}, \quad (2c)$$

$$\psi^{si}(0, y, z) = \psi^{of}(0, y, z) = \psi_s(y, z), \quad (3a)$$

$$\psi^{si}(t_{si}, y, z) = \psi^{ob}(t_{si}, y, z) = \psi_b(y, z), \quad (3b)$$

$$\psi^{si}(x, y, \pm w/2) = \psi^{ow}(x, y, \pm w/2), \quad (3c)$$

$$\left. \frac{\partial \psi^{si}}{\partial x} \right|_{x=0} = \frac{\epsilon_{ox}}{\epsilon_{si}} \left(\left. \frac{\partial \psi^{of}}{\partial x} \right|_{x=0} - \frac{Q_{ssf}}{\epsilon_{ox}} \right), \quad (4a)$$

$$\left. \frac{\partial \psi^{si}}{\partial x} \right|_{x=t_{si}} = \frac{\epsilon_{ox}}{\epsilon_{si}} \left(\left. \frac{\partial \psi^{ob}}{\partial x} \right|_{x=t_{si}} + \frac{Q_{ssb}}{\epsilon_{ox}} \right), \quad (4b)$$

$$\left. \frac{\partial \psi^{si}}{\partial z} \right|_{z=\pm w/2} = \frac{\epsilon_{ox}}{\epsilon_{si}} \left(\left. \frac{\partial \psi^{ow}}{\partial z} \right|_{z=\pm w/2} \pm \frac{Q_{ssw}}{\epsilon_{ox}} \right), \quad (4c)$$

where $\phi_{msf} = \phi_{mf} - \phi_s$ ($\phi_{msb} = \phi_{mb} - \phi_s$) is the difference between the work functions ϕ_{mf} (ϕ_{mb}) for the front (back) gate electrode and ϕ_s for the silicon body; $\psi_s(y, z)$ and $\psi_b(y, z)$ are the front and back surface potentials, respectively; ϵ_{ox} is the dielectric constant of oxide; and Q_{ssf} (Q_{ssb} , Q_{ssw}) is the surface state charge areal density at the front (back, side-wall) oxide/silicon interface. Starting from the potentials obtained by neglecting both $\partial^2 \psi_s(y, z) / \partial y^2 + \partial^2 \psi_s(y, z) / \partial z^2$ and $\partial^2 \psi_b(y, z) / \partial y^2 + \partial^2 \psi_b(y, z) / \partial z^2$, and updating the potential expressions iteratively by taking into account their y - and z -variations, we can write $\psi^{of}(x, y, z)$, $\psi^{ob}(x, y, z)$, and $\psi^{si}(x, y, z)$ in the 3-D expressions:

$$\begin{aligned} \psi^{of}(x, y, z) = & V'_{GS} + \frac{x+t_{of}}{t_{of}} [\psi_s(y, z) - V'_{GS}] \\ & - \frac{1}{3!t_{of}^3} \left[(x+t_{of})^3 - t_{of}^2(x+t_{of}) \right] \Delta_{yz} \psi_s(y, z) \\ & + \frac{1}{5!t_{of}^5} \left[(x+t_{of})^5 - \frac{10}{3} t_{of}^2(x+t_{of})^3 + \frac{7}{3} t_{of}^4(x+t_{of}) \right] \Delta_{yz}^2 \psi_s(y, z) \\ & \dots, \end{aligned} \quad (5)$$

$$\begin{aligned} \psi^{ob}(x, y, z) = & V'_{BS} - \frac{x'-t_{ob}}{t_{ob}} [\psi_b(y, z) - V'_{BS}] \\ & + \frac{1}{3!t_{ob}^3} \left[(x'-t_{ob})^3 - t_{ob}^2(x'-t_{ob}) \right] \Delta_{yz} \psi_b(y, z) \\ & - \frac{1}{5!t_{ob}^5} \left[(x'-t_{ob})^5 - \frac{10}{3} t_{ob}^2(x'-t_{ob})^3 + \frac{7}{3} t_{ob}^4(x'-t_{ob}) \right] \Delta_{yz}^2 \psi_b(y, z) \\ & + \dots, \end{aligned} \quad (6)$$

$$\begin{aligned} \psi^{si}(x, y, z) = & \frac{qN_A}{2\epsilon_{si}} x x' - \frac{x'}{t_{si}} \psi_s(y, z) + \frac{x}{t_{si}} \psi_b(y, z) \\ & + \frac{1}{3!t_{si}^3} (x-x')^3 \Delta_{yz} \psi_s(y, z) - \frac{1}{3!t_{si}^3} (x-x')^3 \Delta_{yz} \psi_b(y, z) \\ & + \frac{1}{5!t_{si}^5} \left[(x-x')^5 - \frac{10}{3} t_{si}^2(x-x')^3 + \frac{7}{3} t_{si}^4(x-x') \right] \Delta_{yz}^2 \psi_s(y, z) \\ & - \frac{1}{5!t_{si}^5} \left[(x-x')^5 - \frac{10}{3} t_{si}^2(x-x')^3 + \frac{7}{3} t_{si}^4(x-x') \right] \Delta_{yz}^2 \psi_b(y, z) \\ & + \dots, \end{aligned}$$

$$\begin{aligned} & + \frac{1}{3!t_{si}^3} (x-x')^3 \Delta_{yz} \psi_s(y, z) - \frac{1}{3!t_{si}^3} (x-x')^3 \Delta_{yz} \psi_b(y, z) \\ & - \frac{1}{5!t_{si}^5} \left[(x-x')^5 - \frac{10}{3} t_{si}^2(x-x')^3 + \frac{7}{3} t_{si}^4(x-x') \right] \Delta_{yz}^2 \psi_s(y, z) \\ & + \frac{1}{5!t_{si}^5} \left[(x-x')^5 - \frac{10}{3} t_{si}^2(x-x')^3 + \frac{7}{3} t_{si}^4(x-x') \right] \Delta_{yz}^2 \psi_b(y, z) + \dots, \end{aligned} \quad (7)$$

where $x' = x - t_{si}$, $\Delta_{yz} = \partial^2 / \partial y^2 + \partial^2 / \partial z^2$, and $\Delta_{yz}^2 = (\partial^2 / \partial y^2 + \partial^2 / \partial z^2)^2$. Using Eqs. (5), (6), and (7), we can rewrite Eqs. (4a) and (4b) as:

$$\begin{aligned} & \left(\frac{1}{t_{si}} + \frac{\eta}{t_{of}} \right) \psi_s(y, z) - \frac{1}{t_{si}} \psi_b(y, z) \\ & - \left(\frac{t_{si}}{3} + \frac{\eta t_{of}}{3} \right) \Delta_{yz} \psi_s(y, z) - \frac{t_{si}}{6} \Delta_{yz} \psi_b(y, z) \\ & - \left(\frac{t_{si}^3}{45} + \frac{\eta t_{of}^3}{45} \right) \Delta_{yz}^2 \psi_s(y, z) - \frac{7t_{si}^3}{360} \Delta_{yz}^2 \psi_b(y, z) - \dots \end{aligned} \quad (8)$$

$$\begin{aligned} & = \frac{\eta}{t_{of}} \left(V_{GS} - V_{FBf} - \frac{qN_A t_{of} t_{si}}{2\epsilon_{ox}} \right), \\ & \left(\frac{1}{t_{si}} + \frac{\eta}{t_{ob}} \right) \psi_b(y, z) - \frac{1}{t_{si}} \psi_s(y, z) \\ & - \left(\frac{t_{si}}{3} + \frac{\eta t_{ob}}{3} \right) \Delta_{yz} \psi_b(y, z) - \frac{t_{si}}{6} \Delta_{yz} \psi_s(y, z) \\ & - \left(\frac{t_{si}^3}{45} + \frac{\eta t_{ob}^3}{45} \right) \Delta_{yz}^2 \psi_b(y, z) - \frac{7t_{si}^3}{360} \Delta_{yz}^2 \psi_s(y, z) - \dots \\ & = \frac{\eta}{t_{ob}} \left(V_{BS} - V_{FBb} - \frac{qN_A t_{ob} t_{si}}{2\epsilon_{ox}} \right), \end{aligned} \quad (9)$$

where $\eta = \epsilon_{ox} / \epsilon_{si}$, $V_{FBf} = \phi_{msf} - t_{of} Q_{ssf} / \epsilon_{ox}$, and $V_{FBb} = \phi_{msb} - t_{ob} Q_{ssb} / \epsilon_{ox}$. For the conditions that Eqs. (8) and (9) satisfy simultaneously, we may suggest that

$$\left(\frac{\partial^2}{\partial y^2} + \frac{\partial^2}{\partial z^2} \right) \psi_s(y, z) = \frac{1}{\lambda_x^2} [\psi_s(y, z) - \Phi_s], \quad (10)$$

$$\left(\frac{\partial^2}{\partial y^2} + \frac{\partial^2}{\partial z^2} \right) \psi_b(y, z) = \frac{1}{\lambda_x^2} [\psi_b(y, z) - \Phi_b]. \quad (11)$$

Then we can rewrite Eqs. (8) and (9) in the forms:

$$\begin{aligned} & \frac{1}{\lambda_x} \left[\cot \left(\frac{t_{si}}{\lambda_x} \right) + \eta \cot \left(\frac{t_{of}}{\lambda_x} \right) \right] [\psi_s(y, z) - \Phi_s] \\ & - \frac{1}{\lambda_x} \csc \left(\frac{t_{si}}{\lambda_x} \right) [\psi_b(y, z) - \Phi_b] \end{aligned}$$

$$\begin{aligned}
&= \frac{\eta}{t_{of}} \left(V_{GS} - V_{FBf} - \frac{qN_A t_{of} t_{si}}{2\epsilon_{ox}} \right) - \left(\frac{1}{t_{si}} + \frac{\eta}{t_{of}} \right) \Phi_s + \frac{1}{t_{si}} \Phi_b, \quad (12) \\
&\frac{1}{\lambda_x} \left[\cot \left(\frac{t_{si}}{\lambda_x} \right) + \eta \cot \left(\frac{t_{ob}}{\lambda_x} \right) \right] [\psi_b(y, z) - \Phi_b] \\
&- \frac{1}{\lambda_x} \csc \left(\frac{t_{si}}{\lambda_x} \right) [\psi_s(y, z) - \Phi_s] \\
&= \frac{\eta}{t_{ob}} \left(V_{BS} - V_{FBb} - \frac{qN_A t_{ob} t_{si}}{2\epsilon_{ox}} \right) - \left(\frac{1}{t_{si}} + \frac{\eta}{t_{ob}} \right) \Phi_b + \frac{1}{t_{si}} \Phi_s, \quad (13)
\end{aligned}$$

where we have replaced $1/\theta - \theta/3 - \theta^3/45 - \dots$ and $1/\theta + \theta/6 + 7\theta^3/360 + \dots$ with $\cot\theta$ and $\csc\theta$, respectively. Combining Eqs. (12) and (13), we can get

$$\begin{aligned}
\Phi_s &= \frac{\eta t_{si} + t_{ob}}{\eta t_{si} + t_{of} + t_{ob}} \left(V_{GS} - V_{FBf} - \frac{qN_A t_{of} t_{si}}{2\epsilon_{ox}} \right) \\
&+ \frac{t_{of}}{\eta t_{si} + t_{of} + t_{ob}} \left(V_{BS} - V_{FBb} - \frac{qN_A t_{ob} t_{si}}{2\epsilon_{ox}} \right), \quad (14)
\end{aligned}$$

$$\begin{aligned}
\Phi_b &= \frac{\eta t_{si} + t_{of}}{\eta t_{si} + t_{of} + t_{ob}} \left(V_{BS} - V_{FBb} - \frac{qN_A t_{ob} t_{si}}{2\epsilon_{ox}} \right) \\
&+ \frac{t_{ob}}{\eta t_{si} + t_{of} + t_{ob}} \left(V_{GS} - V_{FBf} - \frac{qN_A t_{of} t_{si}}{2\epsilon_{ox}} \right), \quad (15)
\end{aligned}$$

$$\cot \left(\frac{t_{si}}{\lambda_x} \right) = \frac{\frac{1}{\eta} - \eta \cot \left(\frac{t_{of}}{\lambda_x} \right) \cot \left(\frac{t_{ob}}{\lambda_x} \right)}{\cot \left(\frac{t_{of}}{\lambda_x} \right) + \cot \left(\frac{t_{ob}}{\lambda_x} \right)}. \quad (16)$$

Since $1/\theta - \theta/3 - \theta^3/45 - \dots = \cot\theta$ is valid in the range of $|\theta| < \pi$, the root of Eq. (16) will be derived uniquely in the range of $\lambda_x > \pi^{-1} \max\{t_{si}, t_{of}, t_{ob}\}$. For a symmetric dual gate (SDG) MOSFET ($t_{of} = t_{ob} = t_{ox}$, $V_{FBf} = V_{FBb} = V_{FB}$, $V_{GS} = V_{BS}$), we can have

$$\Phi_s = \Phi_b = V_{GS} - V_{FB} - \frac{qN_A t_{ox} t_{si}}{2\epsilon_{ox}}, \quad (17)$$

$$\tan \left(\frac{t_{si}}{2\lambda_x} \right) = \eta \cot \left(\frac{t_{ox}}{\lambda_x} \right). \quad (18)$$

In case that $t_{of} \ll \lambda_x$ and $t_{ob} \ll \lambda_x$, Eq. (16) can be simplified as

$$\tan \left(\frac{t_{si}}{\lambda_x} \right) = \frac{\frac{1}{\lambda_x} \left(\frac{t_{of}}{\eta} + \frac{t_{ob}}{\eta} \right)}{\frac{t_{of} t_{ob}}{\eta^2 \lambda_x^2} - 1}, \quad (19)$$

which was used in [14, 17]. Since most of mathematics used in [14, 17] are strictly based on Eq. (19), the models [14, 17] will be very restricted 3-D models. Furthermore, in [14, 17], $\psi^{si}(x, y, z)$ has been expressed as a variable dependent on x in the sinusoidal fashion and on z in the hyperbolic fashion. This expression would not be adequate when considering the fact that x and z are exchangeable, especially in the case of $t_{of} = t_{ob}$.

Meanwhile, considering that $\partial\psi_s(y, z)/\partial z|_{z=0} = 0$ and $\partial\psi_b(y, z)/\partial z|_{z=0} = 0$, and also considering that the z -dependency of $\psi^{si}(x, y, z)$ will be very similar to the x -dependency, we may solve Eqs. (10) and (11), respectively, in the forms:

$$\psi_s(y, z) = \Phi_s + [\psi_{sc}(y) - \Phi_s] \cos \left(\frac{z}{\lambda_z} \right), \quad (20)$$

$$\psi_b(y, z) = \Phi_b + [\psi_{bc}(y) - \Phi_b] \cos \left(\frac{z}{\lambda_z} \right), \quad (21)$$

where

$$\begin{aligned}
\psi_{sc}(y) &= \psi_s(y, 0) = \Phi_s \\
&+ \operatorname{csch} \left(\frac{L}{\lambda} \right) \left\{ [\psi_{sc}(0) - \Phi_s] \sinh \left(\frac{L-y}{\lambda} \right) + [\psi_{sc}(L) - \Phi_s] \sinh \left(\frac{y}{\lambda} \right) \right\}, \quad (22)
\end{aligned}$$

$$\begin{aligned}
\psi_{bc}(y) &= \psi_b(y, 0) = \Phi_b \\
&+ \operatorname{csch} \left(\frac{L}{\lambda} \right) \left\{ [\psi_{bc}(0) - \Phi_b] \sinh \left(\frac{L-y}{\lambda} \right) + [\psi_{bc}(L) - \Phi_b] \sinh \left(\frac{y}{\lambda} \right) \right\}, \quad (23)
\end{aligned}$$

with

$$\lambda = \frac{1}{\sqrt{\lambda_x^2 + \lambda_z^2}}. \quad (24)$$

Then we can rewrite Eqs. (5), (6), and (7) in the forms:

$$\begin{aligned}
\psi^{of}(x, y, z) &= V'_{GS} + \frac{x+t_{of}}{t_{of}} (\Phi_s - V'_{GS}) \\
&+ \csc \left(\frac{t_{of}}{\lambda_x} \right) \sin \left(\frac{x+t_{of}}{\lambda_x} \right) [\psi_{sc}(y) - \Phi_s] \cos \left(\frac{z}{\lambda_z} \right), \quad (25)
\end{aligned}$$

$$\begin{aligned}
\psi^{ob}(x, y, z) &= V'_{BS} - \frac{x'-t_{ob}}{t_{ob}} (\Phi_b - V'_{BS}) \\
&- \csc \left(\frac{t_{ob}}{\lambda_x} \right) \sin \left(\frac{x'-t_{ob}}{\lambda_x} \right) [\psi_{bc}(y) - \Phi_b] \cos \left(\frac{z}{\lambda_z} \right), \quad (26)
\end{aligned}$$

$$\begin{aligned} \psi^{si}(x, y, z) = & \frac{qN_A}{2\varepsilon_{si}} xx' - \frac{x'}{t_{si}} \Phi_s + \frac{x}{t_{si}} \Phi_b \\ & - \csc\left(\frac{t_{si}}{\lambda_x}\right) \sin\left(\frac{x'}{\lambda_x}\right) [\psi_{sc}(y) - \Phi_s] \cos\left(\frac{z}{\lambda_z}\right) \\ & + \csc\left(\frac{t_{si}}{\lambda_x}\right) \sin\left(\frac{x}{\lambda_x}\right) [\psi_{bc}(y) - \Phi_b] \cos\left(\frac{z}{\lambda_z}\right). \end{aligned} \quad (27)$$

On the other hand, we may suggest $\psi^{ow}(x, y, z)$ of the form:

$$\begin{aligned} \psi^{ow}(x, y, z) = & V_{GS}' - \frac{z'}{t_{ow}} \left(\frac{qN_A}{2\varepsilon_{si}} xx' - \frac{x'}{t_{si}} \Phi_s + \frac{x}{t_{si}} \Phi_b - V_{GS}' \right) \\ & + \frac{qN_A}{\varepsilon_{si}} \frac{1}{3!t_{ow}^3} (z'^3 - t_{ow}^2 z') - \csc\left(\frac{t_{ow}}{\lambda_z}\right) \sin\left(\frac{z'}{\lambda_z}\right) \varphi_w(x, y), \end{aligned} \quad (28)$$

where $z' = z \mp (w/2 + t_{ow})$ and

$$\begin{aligned} \varphi_w(x, y) = & -\csc\left(\frac{t_{si}}{\lambda_x}\right) \sin\left(\frac{x'}{\lambda_x}\right) [\psi_{sc}(y) - \Phi_s] \cos\left(\frac{w}{2\lambda_z}\right) \\ & + \csc\left(\frac{t_{si}}{\lambda_x}\right) \sin\left(\frac{x}{\lambda_x}\right) [\psi_{bc}(y) - \Phi_b] \cos\left(\frac{w}{2\lambda_z}\right). \end{aligned} \quad (29)$$

Using Eqs. (27) and (28) into Eq. (4c), we may have

$$\begin{aligned} & \frac{1}{\lambda_z} \left[\tan\left(\frac{w}{2\lambda_z}\right) - \eta \cot\left(\frac{t_{ow}}{\lambda_z}\right) \right] \varphi_w(x, y) \\ & = \frac{\eta}{t_{ow}} \left(\frac{qN_A}{2\varepsilon_{si}} xx' - \frac{x'}{t_{si}} \Phi_s + \frac{x}{t_{si}} \Phi_b - V_{GS}' + V_{FBw} - \frac{qN_A}{3\varepsilon_{si}} t_{ow}^2 \right), \end{aligned} \quad (30)$$

where $V_{FBw} = \phi_{msf} - t_{ow} Q_{ssw} / \varepsilon_{ox}$. Considering that the electrostatic corner effects, we may assume that V_{FBw} is varied with x in order to satisfy that

$$\begin{aligned} & V_{FBw}(x) \\ & = \frac{qN_A}{3\varepsilon_{si}} t_{ow}^2 - \left(\frac{qN_A}{2\varepsilon_{si}} xx' - \frac{x'}{t_{si}} \Phi_s + \frac{x}{t_{si}} \Phi_b - V_{GS}' \right). \end{aligned} \quad (31)$$

Then we have

$$\tan\left(\frac{w}{2\lambda_z}\right) = \eta \cot\left(\frac{t_{ow}}{\lambda_z}\right). \quad (32)$$

Considering that $\theta + \theta^3/3 + 2\theta^5/15 + \dots = \tan(\theta)$ and $1/(2\theta) - 2\theta/3 - (2\theta)^3/45 - \dots = \cot(2\theta)$ can be valid in the range of $|\theta| < \pi/2$, λ_z will be derived uniquely in the range of $\lambda_z > \pi^{-1} \max\{w, t_{ow}\}$. From Eq. (32), we get

$$\frac{\partial \lambda_z}{\partial w} = \frac{\lambda_z}{w + 2t_{ow} \left[\eta + \left(\frac{1}{\eta} - \eta \right) \sin^2\left(\frac{w}{2\lambda_z}\right) \right]}. \quad (33)$$

Using Eqs. (22) and (23) and performing the similar algebraic procedure given in [18, 19], the minimum values of $\psi_s(y, 0)$ and $\psi_b(y, 0)$, i.e., $\psi_{s,\min}$ and $\psi_{b,\min}$ can be derived as

$$\begin{aligned} \psi_{s,\min} = & \Phi_s \\ & + [\psi_{sc}(0) - \Phi_s] \sqrt{1 - \left[\tanh\left(\frac{L}{2\lambda}\right) - \frac{\psi_{sc}(L) - \psi_{sc}(0)}{\psi_{sc}(0) - \Phi_s} \operatorname{csch}\left(\frac{L}{\lambda}\right) \right]^2}, \end{aligned} \quad (34)$$

$$\begin{aligned} \psi_{b,\min} = & \Phi_b \\ & + [\psi_{bc}(0) - \Phi_b] \sqrt{1 - \left[\tanh\left(\frac{L}{2\lambda}\right) - \frac{\psi_{bc}(L) - \psi_{bc}(0)}{\psi_{bc}(0) - \Phi_b} \operatorname{csch}\left(\frac{L}{\lambda}\right) \right]^2}. \end{aligned} \quad (35)$$

In the sub-threshold regime, ohmic drops due to the parasitic source and drain resistances can be negligible. Thus, we may write $\psi_{sc}(0) \approx \psi_{bc}(0) \approx V_{bi}$ and $\psi_{sc}(L) - \psi_{sc}(0) \approx \psi_{bc}(L) - \psi_{bc}(0) \approx V_{DS}$, where V_{bi} is the built-in voltage across the $n^+ - p$ source (drain)/body junction. Let us suggest that the sub-threshold conditions of the front and back surface channels, i.e., $V_{GS} \leq V_{Tf}$ and $V_{BS} \leq V_{Tb}$, are equivalent to $\psi_{s,\min} \leq 2\Phi_B$ and $\psi_{b,\min} \leq 2\Phi_B$, where V_{Tf} and V_{Tb} are the front and back gate threshold voltages, respectively, and $\Phi_B = (k_B T/q) \ln(N_A/n_i)$ is the Fermi potential of the silicon body (k_B is Boltzmann's constant, T is the absolute temperature, and n_i is the intrinsic carrier density of silicon). Then substituting Eqs. (34) and (35) into $\psi_{s,\min} \leq 2\Phi_B$ and $\psi_{b,\min} \leq 2\Phi_B$, respectively, we can have $\Phi_s \leq 2\Phi_B - \zeta$ and $\Phi_b \leq 2\Phi_B - \zeta$, where ζ is given by

$$\begin{aligned} \zeta = & \left(V_{bi} + \frac{1}{2} V_{DS} - 2\Phi_B \right) \operatorname{csch}^2\left(\frac{L}{2\lambda}\right) \\ & + \sqrt{(V_{bi} - 2\Phi_B)(V_{bi} + V_{DS} - 2\Phi_B)} \cosh\left(\frac{L}{2\lambda}\right) \operatorname{csch}^2\left(\frac{L}{2\lambda}\right), \end{aligned} \quad (36)$$

which approaches zero as L is much larger than λ .

Plugging Eqs. (14) and (15) into $\Phi_s \leq 2\Phi_B - \zeta$ and $\Phi_b \leq 2\Phi_B - \zeta$, we can derive finally V_{Tf} and V_{Tb} as

$$V_{Tf} = V_{FBf} + \frac{qN_A t_{of} t_{si}}{2\epsilon_{ox}} + \left(1 + \frac{t_{of}}{\eta t_{si} + t_{ob}}\right) (2\Phi_B - \zeta) - \frac{t_{of}}{\eta t_{si} + t_{ob}} \left(V_{BS} - V_{FBb} - \frac{qN_A t_{ob} t_{si}}{2\epsilon_{ox}} \right), \quad (37)$$

$$V_{Tb} = V_{FBb} + \frac{qN_A t_{ob} t_{si}}{2\epsilon_{ox}} + \left(1 + \frac{t_{ob}}{\eta t_{si} + t_{of}}\right) (2\Phi_B - \zeta) - \frac{t_{ob}}{\eta t_{si} + t_{of}} \left(V_{GS} - V_{FBf} - \frac{qN_A t_{of} t_{si}}{2\epsilon_{ox}} \right). \quad (38)$$

From Eq. (37), we have

$$\frac{\partial V_{Tf}}{\partial L} = \left(1 + \frac{t_{of}}{\eta t_{si} + t_{ob}}\right) \left| \frac{\partial \zeta}{\partial u} \right| \frac{1}{2\lambda}, \quad (39)$$

$$\frac{\partial V_{Tf}}{\partial w} = - \left(1 + \frac{t_{of}}{\eta t_{si} + t_{ob}}\right) \left| \frac{\partial \zeta}{\partial u} \right| \frac{L\lambda}{2\lambda_z^3} \frac{\partial \lambda_z}{\partial w}, \quad (40)$$

where $u = L/(2\lambda)$ and

$$\frac{\partial \zeta}{\partial u} = -2 \left(V_{bi} + \frac{1}{2} V_{DS} - 2\Phi_B \right) \cosh u \operatorname{csch}^3 u - \sqrt{(V_{bi} - 2\Phi_B)(V_{bi} + V_{DS} - 2\Phi_B)} (1 + \cosh^2 u) \operatorname{csch}^3 u. \quad (41)$$

Similarly, $\partial V_{Tb}/\partial L$ and $\partial V_{Tb}/\partial w$ can be written by exchanging t_{of} with t_{ob} in Eqs. (39) and (40). From Eqs. (37) and (38), we find that $\Delta V_{Tf} \equiv V_{Tf} - V_{Tf}|_{L=\infty}$ and $\Delta V_{Tb} \equiv V_{Tb} - V_{Tb}|_{L=\infty}$ are independent of V_{BS} and V_{GS} , respectively. Also note that V_{Tf} and V_{Tb} for a considerably wide gate device ($\lambda_z \approx w/\pi \gg \lambda_x$, i.e., $\lambda \approx \lambda_x$) coincide with the results given in the 2-D analytical model [18].

Let us consider the case that $t_{si} = w$, $t_{of} = t_{ob} = t_{ow}$, and $V_{GS}' = V_{BS}'$. In this case, the device becomes a surrounding gate (gate all around: GAA) MOSFET with rectangular silicon pillar or a fin FET. Using Eqs. (18), (24), and (32), we get

$$\tan\left(\frac{t_{si}}{2\sqrt{2}\lambda}\right) = \eta \cot\left(\frac{t_{ox}}{\sqrt{2}\lambda}\right). \quad (42)$$

Approximating Eq. (42) as the following cubic

equation of $\xi = \lambda^2$:

$$\xi^3 - a_1 \xi^2 - a_2 \xi - a_3 \approx 0, \quad (43)$$

where $\tau = 2t_{ox}/t_{si}$, $a_1 = (t_{si}/2\sqrt{2})^2 (\tau/\eta + \tau^2/3)$, $a_2 = (t_{si}/2\sqrt{2})^4 (\tau/3\eta + \tau^4/45)$, and $a_3 = (t_{si}/2\sqrt{2})^6 (2\tau/15\eta + 2\tau^6/945)$, we can derive λ as

$$\lambda \approx \sqrt{\frac{a_1}{3} + \sqrt[3]{R + \sqrt{Q^3 + R^2}} + \sqrt[3]{R - \sqrt{Q^3 + R^2}}}, \quad (44)$$

where $Q = -(3a_2 + a_1^2)/9$ and $R = (9a_1 a_2 + 2a_1^3 + 27a_3)/54$. Let us assume that a surrounding gate MOSFET with rectangular pillar is somewhat similar to a cylindrical surrounding gate MOSFET with $r_s \approx t_{si}/2$. Then we can find that the derived λ for a rectangular shaped surrounding gate MOSFET becomes very similar to the result given in [20] where λ for a cylindrical surrounding gate MOSFET has been derived. Thus, we may check the validity of Eqs. (20), (21), and (24).

III. SAMPLE COMPUTATION AND DISCUSSION

According to the expressions derived in Sec. II, λ , $\psi_s(y, 0)$, and the front gate threshold voltage V_{Tf} are computed and plotted in Figs. 2-7. In these computations, the following constants are used: $k_B T/q = 0.0259$ [V], $n_i = 1.45 \times 10^{10}$ [cm⁻³], $\epsilon_{ox} = 3.9 \times 8.85 \times 10^{-14}$ [F/cm], $\epsilon_{si} = 11.8 \times 8.85 \times 10^{-14}$ [F/cm], $V_{bi} = 0.55 + \Phi_B$ [V], $\phi_{msf} = \phi_{msb} = -0.55 - \Phi_B$ [V] (n^+ -poly silicon gates are assumed), and $Q_{ssf} = Q_{ssb} = 6.4 \times 10^{-9}$ [C/cm²]. In order to compare the presented model results with the simulated results given in [9], the values of device parameters have been chosen to be the same values used in the models under comparison. Since the presented model implies that 3-D model for a mesa-isolated SOI MOSFET becomes to be very similar to 2-D model in case that w is not very narrow, we have compared our simulation results with the results given in the 2-D model of [9]. Simulation results given in 3-D models of [14, 17] are not compared with the presented model since the results in [14, 17] seem to be very complicated and rather ambiguous. Solving Eqs. (16) and (32) by means of the Newton-Raphson method, we can determine λ_x and λ_z as the corresponding largest values. It is worth noting that λ becomes smaller than λ_x and decreases as

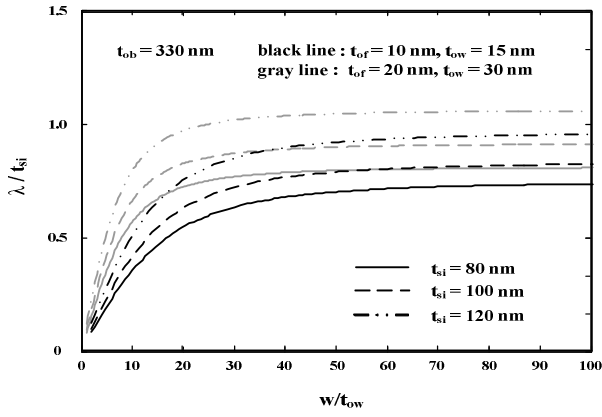


Fig. 2. Derived λ/t_{si} according to Eq. (24) is plotted as a function of w/t_{ow} ($t_{ob} = 330 \text{ nm}$).

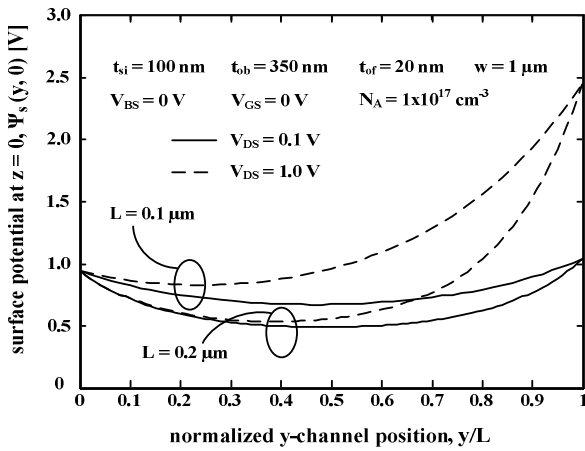


Fig. 3. Surface potential at $z=0$ versus y/L ($w=1 \mu\text{m}$, $t_{si}=100 \text{ nm}$, $t_{ob}=350 \text{ nm}$, $t_{of}=20 \text{ nm}$, $t_{ow}=30 \text{ nm}$, $N_A=1 \times 10^{17} \text{ cm}^{-3}$, $V_{BS}=0 \text{ V}$, $V_{GS}=0 \text{ V}$).

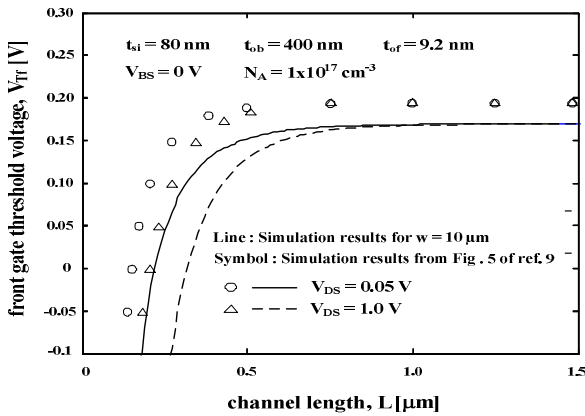


Fig. 4. Front gate threshold voltage versus channel length ($t_{si} = 80 \text{ nm}$, $t_{of} = 9.2 \text{ nm}$, $t_{ob} = 400 \text{ nm}$, $N_A = 1 \times 10^{17} \text{ cm}^{-3}$, $V_{BS} = 0 \text{ V}$).

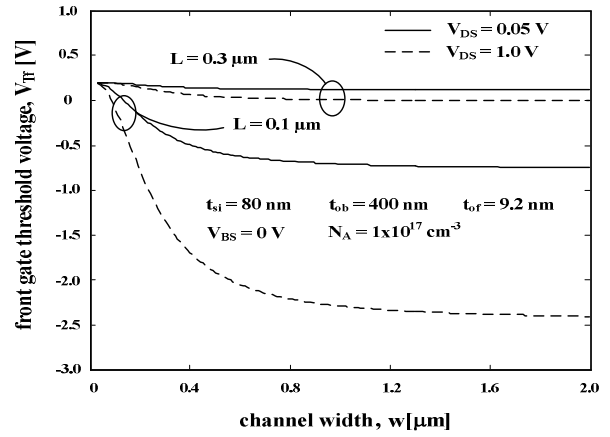


Fig. 5. Front gate threshold voltage versus channel width ($t_{si} = 80 \text{ nm}$, $t_{of} = 9.2 \text{ nm}$, $t_{ob} = 400 \text{ nm}$, $N_A = 1 \times 10^{17} \text{ cm}^{-3}$, $V_{BS} = 0 \text{ V}$).

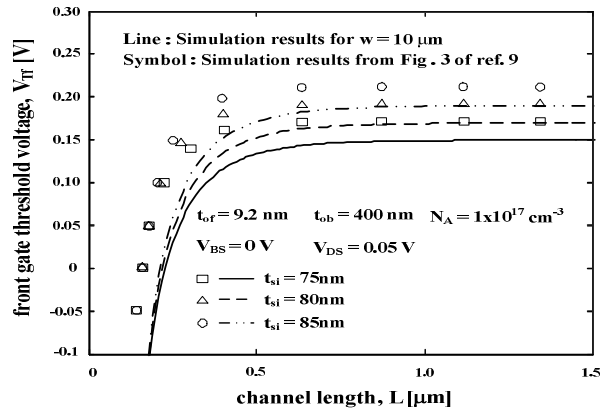


Fig. 6. Front gate threshold voltage versus channel length for various values of silicon film thickness ($t_{si} = 80 \text{ nm}$, $t_{of} = 9.2 \text{ nm}$, $t_{ob} = 400 \text{ nm}$, $N_A = 1 \times 10^{17} \text{ cm}^{-3}$, $V_{BS} = 0 \text{ V}$, $V_{DS} = 0.05 \text{ V}$).

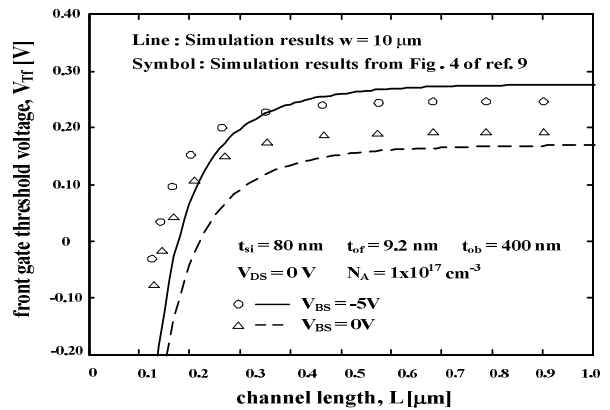


Fig. 7. Front gate threshold voltage versus channel length for $V_{BS} = 0 \text{ V}$ and $V_{BS} = -5 \text{ V}$ ($t_{si} = 80 \text{ nm}$, $t_{of} = 9.2 \text{ nm}$, $t_{ob} = 400 \text{ nm}$, $N_A = 1 \times 10^{17} \text{ cm}^{-3}$, $V_{BS} = 0 \text{ V}$, $V_{DS} = 0 \text{ V}$).

channel width (front gate width) is decreased. In Fig. 2, the derived λ/t_{si} for various values of the silicon film thickness with $t_{ob} = 330 \text{ nm}$ is plotted as a function of w/t_{ow} . The y -variation of $\psi_s(y,0)$ of a device with $L = 0.1 \mu\text{m}$ for $V_{DS} = 0.1 \text{ V}$ and $V_{DS} = 1 \text{ V}$ is plotted in Fig. 3. Also in Fig. 3, the derived y -variation of $\psi_s(y,0)$ of a device with $L = 0.2 \mu\text{m}$ for $V_{DS} = 0.1 \text{ V}$ and $V_{DS} = 1 \text{ V}$ is plotted. The derived dependency of the front gate threshold voltage V_{Tf} on the channel length for $V_{DS} = 0.05 \text{ V}$ and $V_{DS} = 1 \text{ V}$ is plotted in Fig. 4, where the drain-induced threshold voltage roll-off is shown evidently. Derived dependency of the front gate threshold voltage on the channel width for $V_{DS} = 0.05 \text{ V}$ and $V_{DS} = 1 \text{ V}$ is shown in Fig. 5. Derived dependencies of the front gate threshold voltage on the channel length for various values of silicon body thickness and back gate voltage are shown in Fig. 6 and 7, respectively. The simulation results from the presented model show a fairly good agreement with the results from [9], even though very small discrepancy about $\Delta V_T \approx 0.02 \text{ V}$ is also found. Since this discrepancy is almost constant within the wide range of channel length, this would have resulted from the flat-band voltage shift. In the presented model, the potentials can be derived three-dimensionally as functions of infinite order of x and z , while in the previously suggested 2-D model [18] the potentials were derived as functions of fifth-order of x . Considering that the drain-induced threshold voltage roll-off can be described accurately from a rigorous derivation of the front and back surface potentials, it can be expected that the presented model seems to derive the drain-induced threshold voltage roll-off fairly accurately.

IV. CONCLUSIONS

As shown in Section II, starting from the potential expressions obtained by using the GCA and updating them iteratively by taking into account their y - and z - variations, we can derive three-dimensionally the potentials in the silicon film (body), front, back, and side-wall oxide layers of a mesa-isolated fully depleted SOI MOSFET. From the derived three dimensional (3-D) potential expressions, we can derive the minimum values of the front and back surface potentials as closed-form expressions. These closed-form expressions seem to be much more convenient than the series expressions. Finally, using the reasonable physics-based threshold

criterion, the front and back gate threshold voltages can be derived as the closed-form expressions in terms of various device parameters and applied bias voltages. It is worth noting that the presented model uses fewer assumptions and approximations in the analytical model procedure. Obtained results can be found to explain the drain-induced threshold voltage roll-off and the narrow width effect in a unified manner. Consequently, the threshold voltage expressions derived in the presented model can be used for estimating the front and back gate threshold voltages of a short channel fully depleted SOI MOSFET within a range of satisfactory accuracy.

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REFERENCES

- [1] Lim H. Fossum J. R., "Threshold Voltage of Thin-Film Silicon-on-Insulator (SOI) MOSFET's", *IEEE Trans. Electron Devices*, vol. 30, no. 10, pp. 1244, 1983.
- [2] Veeraghavan S. and Fossum J. G., "Short-channel effects in SOI MOSFET's", *IEEE Trans. Electron Devices*, vol. 36, no. 3, pp. 522, 1989.
- [3] R. H. Yan, A. Ourmazd, K. F. Lee, "Scaling the Si MOSFET: from bulk to SOI to bulk," *IEEE Trans. Electron Devices*, vol. 39, no. 7, pp. 1704-1710, 1992.
- [4] P. Francis, A. Terao, D. Flandre, F. Van de Wiele, "Modeling of ultrathin double-gate nMOS/SOI transistors," *IEEE Trans. Electron Devices*, vol. 41, no. 5, pp. 715-720, 1994.
- [5] T. C. Hsiao, J. C. S. Woo, "Subthreshold characteristics of fully depleted submicrometer SOI MOSFET's," *IEEE Trans. Electron Devices*, vol. 42, no. 6, pp. 1120-1125, 1995.
- [6] K. K. Young, "Short-channel effects in fully depleted SOI MOSFET's," *IEEE Trans. Electron Devices*, vol. 36, no. 2, pp. 399-402, 1989.
- [7] J. C. S. Woo, K. W. Terrill, P. K. Vasudev, "Two-dimensional analytical modeling of very thin SOI MOSFET's," *IEEE Trans. Electron Devices*, vol. 37, no. 9, pp. 1999-2000, 1990.
- [8] Joachim H., Yamaguchi Y., Ishikawa K., Inoue Y., and Nishimura T., "Simulation Two-Dimensional analytical modeling of sub-threshold slope in ultrathin-film SOI MOSFET's down to $0.1 \mu\text{m}$ gate length", *IEEE Trans. Electron Devices*, vol. 40, no.

- 10, pp. 1812, 1993.
- [9] S. R. Banna, P. C. H. Chan, P. K. Ko, C. T. Nguyen, and M. Chan, "Threshold Voltage Model for Deep-Submicrometer Fully Depleted SOI MOSFET's," *IEEE Trans. Electron Devices*, vol. 42, no. 11, pp. 1949-1955, 1995.
- [10] J. Y. Guo, C. Y. Wu, "A new 2D analytical threshold-voltage model for fully depleted short-channel SOI MOSFET's," *IEEE Trans. Electron Devices*, vol. 40, no. 9, pp. 1653-1661, 1993.
- [11] K. Suzuki, Y. Tosaka, T. Sugii, "Analytical threshold voltage model for short channel double-gate MOSFET's," *IEEE Trans. Electron Devices*, vol. 43, no. 7, pp. 1166-1168, 1996.
- [12] R. Zhang, K. Roy, "Low-Power high-performance Double-Gate Fully Depleted SOI Circuit Design," *IEEE Trans. Electron Devices*, vol. 49, no. 5, pp. 852-862, 2002.
- [13] K. M. Chang, H. P. Wang, "A simple 2D analytical threshold voltage model for fully depleted short-channel silicon-on-insulator MOSFET's," *Semi-con Sci. Tech*, vol. 19, no. 12, pp. 1397-1405, 2004.
- [14] G. Katti, N. DasGupta, and A. DasGupta, "Threshold Voltage Model for Mesa-Isolated Small Geometry Fully Depleted SOI MOSFETs Based on Analytical Solution of 3-D Poisson's Equation," *IEEE Trans. Electron Devices*, vol. 51, no. 7, pp. 1169-1177, 2004.
- [15] Wenwei Yang, Zhiping Yu, Lilin Tian, "Scaling Theory for FinFETs Based on 3-D Effects Investigation", *IEEE Trans. Electron Devices*, vol. 54, no. 5, 2007.
- [16] Romain Ritzenthaler, Francois Lime, Olivier Faynot, Sorin Cristoloveanu, Benjamin Ininguez, "3D analytical modeling of subthreshold characteristics in vertical Multiple-gate FinFET transistors", *Solid-State Electronics*, 2011.
- [17] Krishna Meel, R. Gopal, Deepak Bhatnagar, "Three-dimensional analytical modelling of front and back gate threshold voltages small geometry Fully Depleted SOI MOSFETs," *Solid-State Electronics*, vol. 62, pp. 174-184, 2011.
- [18] C. H. Suh, "A simple analytical model for the front and back gate threshold voltages of a fully-depleted asymmetric SOI MOSFET," *Solid-State Electronics*, vol. 52, pp. 1249-1255, 2008.
- [19] C. H. Suh, "Analytical model for deriving the threshold voltage of a short gate SOI MESFET with vertically non-uniformly doped silicon film", *IET Circuits, Devices, and Systems*, vol. 4, Iss. 6, pp. 525-530, 2010.
- [20] C. H. Suh, "Two-Dimensional Analytical Model

for Deriving the Threshold Voltage of a Short Channel *Fully Depleted Cylindrical/Surrounding Gate MOSFET*", *Journal of Semiconductor Technology and Science*, vol. 11, no. 2, pp. 111-120, 2011.



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