# Pull-In Voltage Modeling of Graphene Formed Nickel Nano Electro Mechanical Systems (NEMS)

Songnam Lim<sup>1</sup>, Jong-Ho Lee<sup>2</sup> Woo Young Choi<sup>3</sup>, and Il Hwan Cho<sup>1,\*</sup>

Abstract—Pull-in voltage model of nano-electromechanical system with graphene is investigated for the device optimization. In the pull in voltage model, thickness of graphene layer is assumed to be uniform in vertical and lateral direction. Finite element analysis simulation has verified the feasibility of the suggested model. From the suggested model, pull-in voltage change with graphene thickness and cantilever length can be estimated. Maximum induced stress and graphene thickness have a reciprocal relationship.

*Index Terms*—Nano electro mechanical systems (NEMS), graphene, modeling of graphene

## **I. INTRODUCTION**

Nano electro mechanical systems (NEMS) are considered as promising switching devices for a variety of electronic applications due to the zero leakage current and abrupt switching properties [1-6].

However metal based NEMS technologies have some problems such as static friction and abrasion that cause a failure of the electrical contact [7-9]. Graphene is theoretically suited for NEMS and has excellent mechanical properties, including high stiffness and low mass [10-13]. Graphene based NEMS overcome the static friction and help solving reliability problem of micro-electronic devices.

The integration research of graphene NEMS for high density circuits needs selective graphene growth methods. To solve the integration problem, graphene NEMs have been fabricated by using chemical vapor deposition (CVD) on nickel cantilever [14]. The precise control of graphene material on NEMs can be realized by the selective growth of graphene on the pre-fabricated metal beam.

Since the manufacturing of NEMS requires high cost and difficult processes, design optimization is necessary to reduce cost and trial-and-error time [15]. Pull-in voltage can be treated as the most important parameter for NEMS operation because it determines operation voltage and power dissipation. Prediction of pull in voltage with device dimensions can offer a guide line for NEMS design. In this paper, we proposed theoretical models for pull in voltage of NEMS, which is based on graphene with Ni cantilever.

## **II. SIMULATION STRUCTURE**

In this work, NEMS simulations are performed with finite element analysis simulation. In the simulation work, cantilever is assumed to have rectangular parallelepiped beam structure as shown in Fig. 1(a). Fig. 1(b) shows dimensional parameters at the side view of cantilever. One of the cantilever's surface is fixed and another surface moves down to electrode. Fig. 1(c) shows the front side structure parameters of the cantilever for pull-in voltage modeling.

 $L_{beam}$ ,  $T_{beam}$  and  $W_{beam}$  are the cantilever length, thickness and width respectively. G is the distance

Manuscript received May. 22, 2015; accepted Aug. 12, 2015

 <sup>&</sup>lt;sup>1</sup> Department of Electronic Engineering, Myongji University, Yongin, Gyeonggi 449-728, Republic of Korea
 <sup>2</sup> School of Electrical Engineering and Computer Science, Seoul

National University, Seoul 151-742, South Korea

<sup>&</sup>lt;sup>3</sup>Department of Electronic Engineering, Sogang University, Seoul 121-742, Republic of Korea

E-mail: ihcho77@mju.ac.kr



**Fig. 1.** (a) Vertical and lateral schematic diagram of NEMS with grapheme, (b) The equivalent structure of NEMS with graphene and dimensional parameters.

between the cantilever and electrode.  $E_{Graphene}$  is the Young's modulus of graphene and the value of this parameter is cited from the previous work [16].  $T_{Graphene\_TOP}$  and  $W_{Graphene\_Top}$  represent the graphene upper region thickness and width respectively.  $E_i$  is the Young's modulus of nickel.  $T_{Ni}$  and  $W_{Ni}$  are the nickel region thickness and width.

Since two different types of material properties are tied together, mathematical method for equivalent material is necessary to get the pull-in voltage. The moment inertia of two materials is merged into single structure for unified equation. It should be modified as shown in Fig. 1(c). The modified width of Ni is determined by the ratio of Young's modulus of Ni to graphene as shown in Eq. (1) [17-19].

$$n = \frac{E_{Ni}}{E_{Graphene}} W_{Bottom} = n W_{Bottom}$$

$$= \frac{E_{Ni}}{E_{Graphene}} W_{Bottom}$$
(1)

To determine the moment of inertia at the center of the equivalent structure ( $P_{eq}$ ),  $h_1$  and  $h_2$  are defined in an equivalent material structure through the Eq. (2) [19].

$$h_{1} = \frac{\sum yiAi}{\sum Ai}$$

$$= \frac{\frac{1}{2}T_{Top}(T_{Top}W_{Top}) + (T_{Top} + \frac{1}{2}T_{Bottom})\frac{E_{Ni}}{E_{Graphene}}W_{Bottom}T_{Bottom}}{W_{Top}T_{Top} + \frac{E_{Ni}}{E_{Graphene}}W_{Bottom}T_{Bottom}}$$

$$= \frac{\frac{1}{2}T_{Top}^{2}W_{Top} + (T_{Top} + \frac{1}{2}T_{Bottom})W_{Bottom}T_{Bottom}}{W_{Top}T_{Top} + W_{Bottom}T_{Bottom}}$$

$$h_{2} = T_{Top} + T_{Bottom} - h_{1}$$
(2)

 $T_{Top}$  and  $T_{Bottom}$  are top region and bottom region thicknesses respectively as shown in Fig. 1(c).

 $Y_i$  is the distance from  $P_{top}$  to the bottom of the cantilever and  $A_i$  is the cross-sectional area.

Since the direction of motion of the two materials is the same, the moment of inertia of the equivalent structure can be determined by the moments of inertia,  $I_1$ and  $I_2$ . Equation for moments of inertia is as follow Eq. (3).

$$I' = I_1 + I_2$$
 (3)

 $I_1$  is the moment of inertia of the top region of cantilever beam and  $I_2$  is the moment of inertia of the bottom region of cantilever beam.

The moment of inertia of a rectangular structure is expressed by Eq. (4) [19].

$$I = \frac{Wt^3}{12} \tag{4}$$

The moment of inertia, I1 and I2 are calculated as

$$I_1 = I_{1c} + A_1 d_1^2, \quad I_2 = I_{2c} + A_2 d_2^2 \tag{5}$$

 $I_{1c}$  and  $I_{2c}$  mean the moments of inertia of the original structures and  $A_1$  and  $A_2$  are the cross-sectional areas of the equivalent structure.  $d_1$  and  $d_2$  are distances between equivalent structure center and the original structure center at top region and bottom region respectively as shown in Fig. 1(c) [19].

From Eqs. (2-5) the total moment inertia of equivalent structure can be expressed as Eq. (6).

$$I' = \frac{1}{12} W_{Top} T_{Top}^{3} + W_{Top} T_{Top} (h_{1} - \frac{1}{2} T_{Top})^{2} + \frac{1}{12} \frac{E_{Ni}}{E_{Graphene}} W_{Bottom} T_{Bottom}^{3} + \frac{E_{Ni}}{E_{Graphene}} W_{Bottom} T_{Bottom} (h_{2} - \frac{1}{2} T_{Bottom})^{2}$$
(6)

The spring constant of the cantilever with a rectangular cross section is expressed in Eq. (7) [20].

$$k_c = \frac{2}{3} EW\left(\frac{t}{l}\right)^3 \tag{7}$$

The spring constant can be expressed using the moment of inertia by Eq. (8) [20].

$$k_{c} = Ix = \frac{wt^{3}}{12}x = \frac{2}{3}EW\left(\frac{t}{l}\right)^{3}, \quad x = \frac{8E}{l^{3}}, \quad k_{c}\frac{8EI}{l^{3}}$$
(8)

By using the moment of inertia of the equivalent structure from Eq. (6), the modified spring constant is expressed as Eq. (9).

$$k_{c}^{'} = \frac{8E_{Graphene}I^{'}}{L_{Graphene}^{3}}$$
(9)

The pull-in voltage of the cantilever is calculated by using Eq. (10) [21].

$$V_{PI} = \sqrt{\frac{8k_c}{27\varepsilon_0 W_{Top} L_{Top}}} G^3 \tag{10}$$

Here  $\varepsilon_0$  is the vacuum permittivity and G is the distance between the cantilever and the electrode.

The final equation for pull-in voltage for cantilever is expressed as Eq. (11).

$$V_{PI} = \sqrt{\frac{\left[\frac{1}{12}W_{Top}T_{Top}^{3} + W_{Top}T_{Top}(h_{1} - \frac{1}{2}T_{Top})^{2} + \frac{1}{12}\frac{E_{Ni}}{E_{Graphene}}W_{Bottom}T_{Bottom}^{3} + \frac{E_{Ni}}{E_{Graphene}}W_{Bottom}T_{Bottom}(h_{2} - \frac{1}{2}T_{Bottom})^{2}\right]}_{27\varepsilon_{0}W_{Top}L_{Top}^{4}}G^{3}}$$
(11)

## **IV. RESULTS AND DISCUSSIONS**

Fig. 2 shows comparison results between simulation and model when they have variation in graphene width and thickness. The width and thickness variation of graphene shows quadratic relationship with pull-in voltage. Even though there is some quantitative differences between the model and simulation results, the tendency of the pull-in voltages are matched with each other as shown in Fig. 2. Effect of graphene deposition thickness is shown in Fig. 2. From Eq. (11), pull-in voltage slightly increases with graphene thickness. Similar is the result in Fig. 2, difference between model and simulation results increase with graphene thickness. Because the thick graphene can cause high pull up voltage operation, our model can be applied to NEMS modeling with thin graphene.

The difference between simulation and model result is caused by limitation of estimation method [22]. The analytical model assumes a flat cantilever and electrostatic force is assumed to be uniformly distributed over it. Due to uniform electrostatic force distribution, the pull in voltage difference is inversely proportional to the beam length as shown in Fig. 3. Additional research is needed to reduce the quantitative difference between simulation and modeling result.

Although the aggressive scaling down of beam length is hard to be achieved in real fabrication. The difference between model and simulation results is tolerable in real fabrication. In the optimization of NEMS design,



**Fig. 2.** Pull in voltage comparison between suggested model and FEA simulation result in accordance with graphene thickness variation. Cantilever length is fixed at 10 nm.

![](_page_3_Figure_1.jpeg)

**Fig. 3.** Pull in voltage comparison between suggested model and FEA simulation result in accordance with cantilever length variation. Graphene thickness is fixed at 10 nm.

![](_page_3_Figure_3.jpeg)

Fig. 4. Maximum stress values on NEMS accordance with graphene thickness variation.

graphene thickness should be carefully considered. Even though thin graphene NEMS has small pull in voltage but thin graphene can also causes high stress in NEMS. Fig. 4 shows maximum stress of NEMS with graphene thickness variation. Every NEMS has 200 nm length and 1 Pa force is induced to flexible surface of NEMS. As shown in Fig. 4, maximum stress on NEMS decreases with graphene thickness. From this result, thickness of NEMS should be optimized with careful consideration of pull-in voltage and reliability.

# V. CONCLUSIONS

In this paper, analytical modeling of NEMS pull-in voltage has been proposed for the accurate evaluation of

NEMS operation. In particular, the modeling is performed to NEMS device which uses a Ni cantilever as a catalyst of graphene layer. The suggested pull-in voltage model for the NEMS provides a good accuracy when NEMS has long length and thin graphene. Pull in voltage modeling indicates that the graphene thickness has a significant role in determining the pull-in voltage and reliability of NEMS. The graphene thickness should be carefully considered for NEMs design optimization. As graphene thickness increases, pull-in voltage increases. However increase of graphene thickness reduces maximum stress intensity in NEMS cantilever. Our model and simulation results can offer insights of graphene NEMS device optimization.

### ACKNOWLEDGMENTS

The research was partly supported by the ICT R&D program of MSIP/IITP, Republic of Korea (#B0101-15-1347). This research was also partly supported by Basic Science Research Program through the National Research Foundation of Korea (NRF) funded by the Ministry of Education, Science and Technology (No. 2014R1A1A1006439).

#### REFERENCES

- Meiyong Liao, Shunichi Hishita, Eiichiro Watanabe, Satoshi Koizumi and Yasuo Koide, "Suspended Single-Crystal Diamond Nanowires for High-Performance Nanoelectromechanical Switches", Adv. Mater. 22, 5393, 2010
- [2] V Cimalla, J Pezoidt and O Ambacher, "Group III nitride and SiC based MEMS and NEMS: materials properties", technology and applications, Appl. Phys. 40, 20, 2007
- [3] Huiwen Liu, Bharat B, "Nanotribological characterization of molecularly thick lubricant films for applications to MEMS/NEMS by AFM", Ultramicroscopy. 97, 321, 2002
- [4] Dominik V. Scheible, Hua Qin, Hyun-Seok Kim and Robert H. Blick, "Fabrication of doped nanoelectromechanical systems", Phy. Stat. Sol. 1, 205, 2007
- [5] Marie K. Tripp, Christoph Stampfer, David C. Miller, Thomas Helbling, Cari F. Herrmann,

Christofer Hierold, Ken Gall, Steven M. George and Vitor M. Bright, "The mechanical properties of atomic layer deposited alumina for use in microand nano-electromechanical systems", Sen. and Actu. A, 130, 2006

- [6] Michael B. Henry and Leyla Nazhandali, "NEMS-Based Functional Unit Power-Gating: Design, Analysis", and Optimization, IEEE Tran. Cir. And Sys. 60, 290, 2013
- [7] Anisoara Socoliuc, Enrico Gnecco, Sabine Maier, Oliver Pfeiffer, Alexis Baratoff, Roland Bennewitz, Ernst Meyer, "Atomic-Scale Control of Friction by Actuation of Nanometer-Sized Contacts", Science 313, 207, 2006
- [8] Zhiwen Shi, Hongliang Lu, Lianchang Zhang, Rong Yang, Yi Wang, Donghua Liu, Haiming Guo, Dongi shi, Hongjun Gao, Enge Wang, "Studies of graphene-based nanoelectromechanical switches", Nano Research. 5, 2, 2012
- [9] Oded Ben-David and Jay Fineberg, "Static Friction Coefficient Is Not a Material Constant", Phy. Rev. Let. 11, 254301, 2011
- [10] Karumbaiah N. Chappanda and Massood Tabib,
   "Novel graphene bridge for NEMS based devices", IEEE Sensors. 978, p 1358, 2011
- [11] Changyao Chen, Sami Rosenblatt, Kirill I. Bolotin, Philip Kimm Ioannis Kymissism Horst L. Stormer, Tony F, Heinz, James Honem, "NEMS applications of graphene", IEDM. 253, 10, 2009
- [12] Pankaj Sharma, Julien Perruisseau Carrier and Adrian. M. Ionescu, "Graphene NanoElectroMechanical Resonators and Oscillators", ULIS. 14, 2013
- [13] Vibhor Singh and Mandar M. Deshmukh, "Nanoelectromechanics using graphene", Cur. Sci. 107, 437, 2014
- [14] Byeong-In Choe, Jung-kyu Lee, Bora Lee, Kwanyong Kim, Woo Young Choi, Byung Hee Hong and Jong-Ho Lee, "Fabrication and Electrical Characterization of Graphene Formed Chemically on Nickel Nano Electro Mechanical System (NEMS) Switch", J.Nanosci. Nanotechnol 14, 12, 2014
- [15] Marc Dequesnes, S V Rotkin and N R Aluru, "Calculation of pull-in voltages for carbonnanotube-based nanoelectromechanical switches", Nanotechnology. 13, 1, 2002

- [16] Changgu Lee, Xiaoding Wei, Jeffrey W. Kysar, James Hone, "Measurement of the Elastic Properties and Intrinsic Strength of Monolayer Graphene", SCIENCE. 321, 18, 2008
- [17] Woo Young CHOI, Min SU HAN, Boram HAN, Dongsun SEO, IL Hwan CHO, "Modeling of Triangular Sacrificial Layer Residue Effect in Nano-Electro-Mechanical Nonvolatile Memory", IEICE TRANS. ELECTRON. E96, 5, 2013
- [18] Yong Joo Jee and Il Hwan Cho, "Modeling of Sacrificial Layer Residue Effect in Nano-Electro-Mechanical Nonvolatile Memory", JJAP. 50, 100205, 2011
- [19] James. M. Gere, "Mechanics of Materials 5<sup>th</sup> ed.", Chap. 6, Brooks-Cole, Pacific Grove, 2001.
- [20] Gabriel. M. Reviez, "RF MEMS Chap. 2, Wiley", New York, 2003
- [21] Weon Wi Jang, Jun-Bo Yoon, Min-Sang Kim, Ji-Myoung Lee, Sung-Min Kim, Eun-Jung Yoon, Keun Hwi Cho, Sung-Young Lee, In-Hyuk Choi, Dong-Won Kim, Donggun Park, "NEMS switch with 30 nm-thick beam and 20 nm-thick air-gap for high density non-volatile memory applications", Solid-State Electron. 52 (2008) 1578.
- [22] Seung Hyeun Roh, Kwangsoo Kim and Woo Young Choi, "Scaling Trend of Nanoelectromechanical (NEM) Nonvolatile Memory Cells Based on Finite Element Analysis (FEA)", IEEE TRANS-ACTIONS ON NANOTECHNOLOGY. 10, 3, 2011

![](_page_4_Picture_19.jpeg)

**Songnam Lim** was born in seoul, korea, in 1988. He received the B.S. degree in Electronic Engineering from Myonggi University, Yongin, korea, in 2014.He is currently working towards a M.S. degree at the same university.His research interests

include modeling of nanoelectro mechanical memory and Junctionless FET.

![](_page_5_Picture_1.jpeg)

Jong-ho Lee (SM'01) received the B.S. degree from Kyungpook National University, Daegu, Korea, in 1987 and the M.S. and Ph.D. degrees from Seoul National University, Seoul, in 1989 and 1993, respectively, all in electronic engi-

neering. In 1993, he worked on advanced BiCMOS process development at ISRC, Seoul National University as an Engineer. In 1994, he was with the School of Electrical Engineering, Wonkwang University, Iksan, Chonpuk, Korea. In 2002, he moved to Kyungpook National University, Daegu Korea, as a Professor of the School of Electrical Engineering and Computer Science. Since September 2009, he has been a Professor in the School of Electrical and Computer Engineering, Seoul National University, Seoul Korea. From 1994 to 1998, he was with ETRI as an invited member of technical staff, where he worked on deep submicron MOS devices, device isolation. From August 1998 to July 1999, he was with Massachusetts Institute of Technology, Cambridge, as a postdoctoral fellow, where he was engaged in the research on sub-100 nm double-gate CMOS devices. He has authored or coauthored more than 216 papers published in refereed journals and over 326 conference papers related to his research and has been granted 85 patents in this area. His research interests include CMOS technology, non-volatile memory devices, thin film transistors, sensors, neuromorphic technology, and device characterization and modeling. Prof. Lee is a Lifetime Member of the Institute of Electronics Engineers of Korea (IEEK). He has been served as a subcommittee member of IEDM, ITRS ERD member, a general chair of IPFA2011, and IEEE EDS Korea chapter chair. He received 18 awards for excellent research papers and research excellence. He invented bulk FinFET, Saddle FinFET (or bCAT) for DRAM cell, and NAND flash cell string with virtual source/drain, which have been applying for mass production.

![](_page_5_Picture_4.jpeg)

**Woo Young Choi** received the B.S., M.S. and Ph. D. degrees in the School of Electrical Engineering from Seoul National University, Seoul, Korea in 2000, 2002 and 2006, respectively. From 2006 to 2008, he was with the Department of Electrical

Engineering and Computer Sciences, University of California, Berkeley, USA as a post-doctor. Since 2008, he has been a member of the faculty of Sogang University (Seoul, Korea), where he is currently an Associate Professor with the Department of Electronic Engineering. He has authored or coauthored over 180 papers in international journals and conference proceedings and holds 30 Korean patents. His current research interests include fabrication, modeling, characterization and measurement of CMOS/CMOSsemiconductor compatible devices and nanoelectromechanical (NEM) devices.

![](_page_5_Picture_7.jpeg)

**II Hwan Cho** was born in Anyang, Korea, in 1972. He received the B.S. in Electrical Engineering from Korea Advanced Institute of Science and Technology(KAIST), Daejon, Korea, in 2000 and M.S., and Ph.D. degrees in electrical engineering from Seoul

National University, Seoul, Korea, in 2002, 2007, respectively. From March 2007 to February 2008, he was a Postdoctoral Fellow at Seoul National University, Seoul, Korea, where he was engaged in the research on characterization of bulk finfet SONOS flash memory. In 2008, he joined the Department of Electronic Engineering at Myongji University, Yongin, where he is currently an Associate Professor. His current research interests include improvement, characterization and measurement of non-volatile memory devices and semiconductor devices with high-k layer.